

# AMD UMA/PX Schematics

## AMD Stoney Ridge 15h models 70h-7Fh\_FP4 type III.

### AMD R16M GPU Family R16M-M1-30 S3 Package

#### SA

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Title

**001 Cover Page**

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A4

Document Number

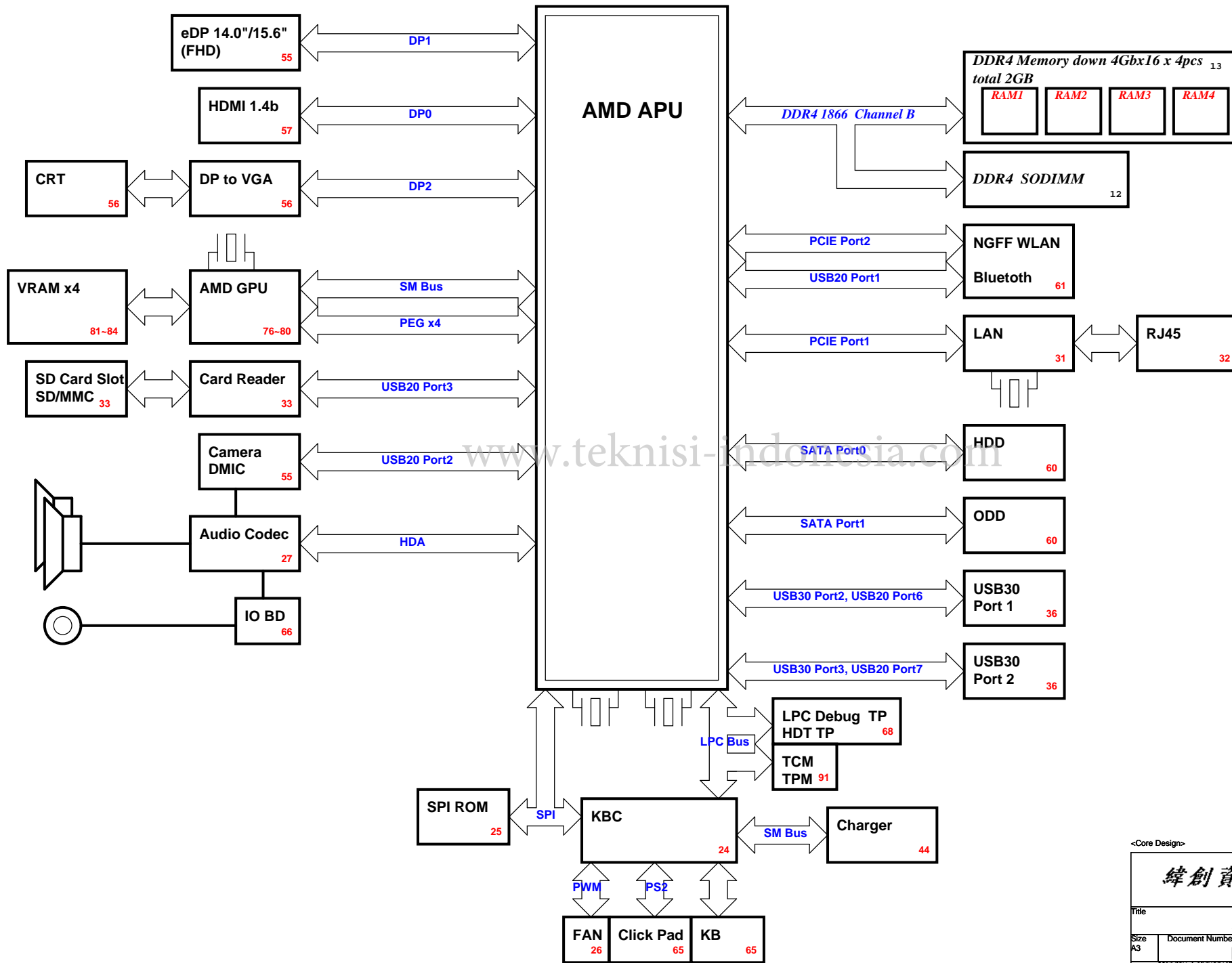
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# Stoney Ridge Block Diagram

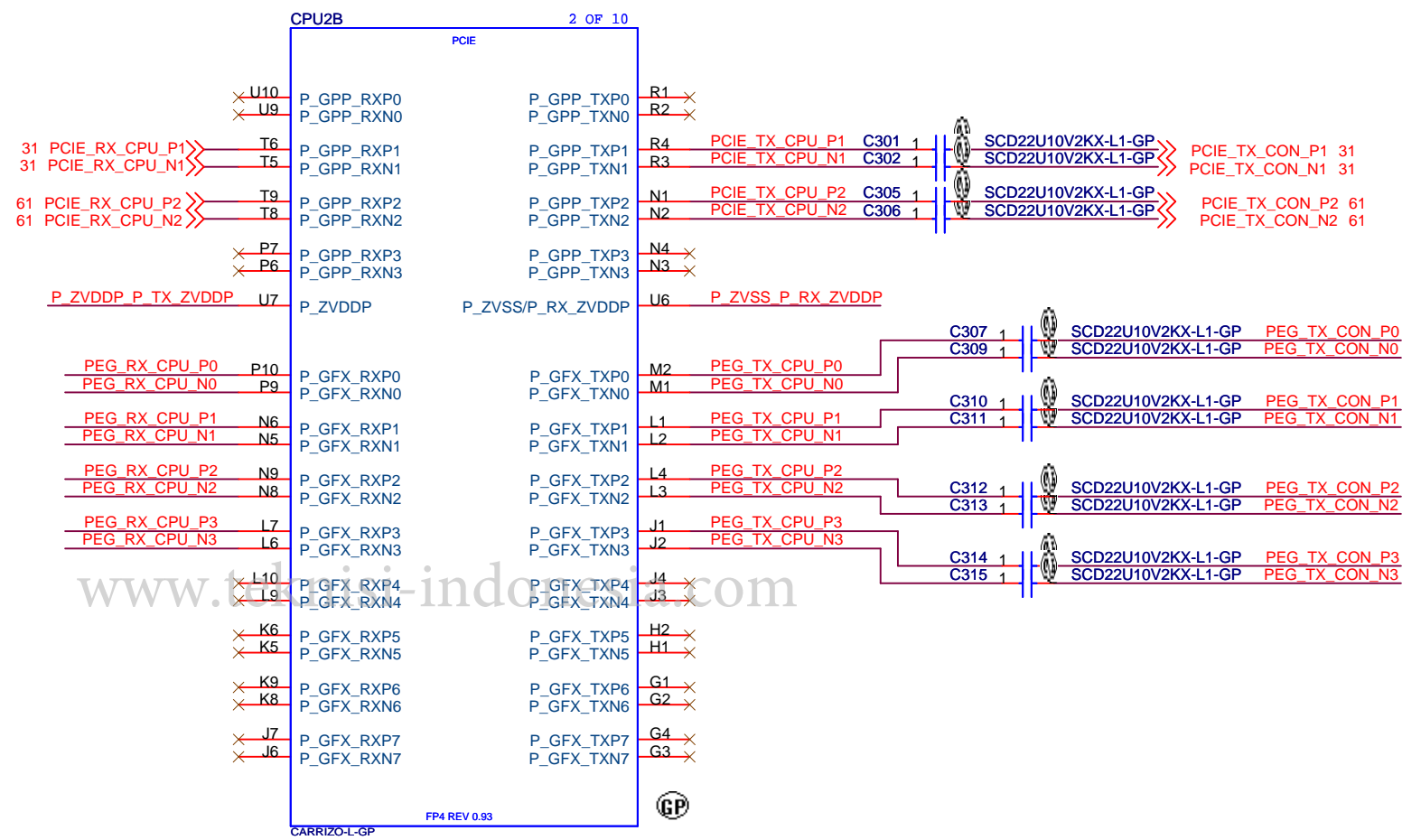
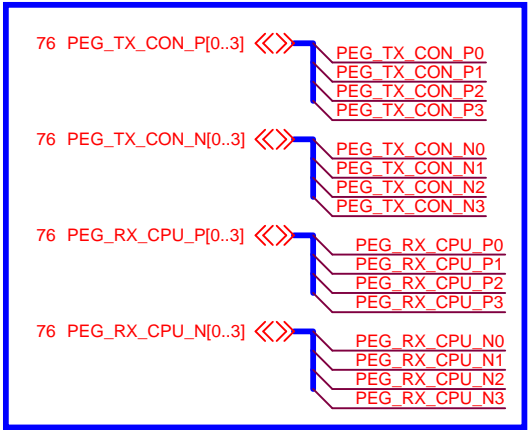
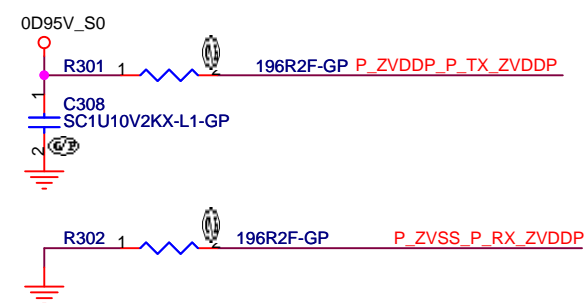


<b>CHARGER</b>	
ISL95520HRZ	44
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	BT+
<b>SYSTEM DC/DC</b>	
TPS51275CRUKR	45
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	5V_S5 3D3V_S5
<b>CPU DC/DC</b>	
RT8179C6QW	46-48
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	1V_CPU_CORE APU_VDDNB
<b>CPU DC/DC</b>	
RT8179C6QW	49-50
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	VDD6FX_CORE
<b>SYSTEM DC/DC</b>	
APL5337KAI	50
<b>INPUTS</b>	<b>OUTPUTS</b>
1D8V_S5	0D775V_S5
<b>SYSTEM DC/DC</b>	
TPS51716RUKR	
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	1D35V_CPU_VDDQ_S3 0D675V_S0
<b>SYSTEM DC/DC</b>	
AOZ1268QI	51
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	0D95V_S5
<b>SYSTEM DC/DC</b>	
RT85096QW	53
<b>INPUTS</b>	<b>OUTPUTS</b>
5V_S0	15V_LCD
<b>SYSTEM DC/DC</b>	
ISL6277IHRZ	85
<b>INPUTS</b>	<b>OUTPUTS</b>
19V_DCBATOUT	1V_VGACORE_S0
<b>Switches</b>	
40	86
<b>INPUTS</b>	<b>OUTPUTS</b>
APU_VDDNB 0D775V_S5	VDDCR_FCH_S5
3D3V_S0	3D3V_VGA_S0
1D8V_S0	1D8V_VGA_S0
0D95V_S0	0D95V_VGA_S0
1D35V_CPU_VDDQ_S3	1D35V_VGA_S0
<b>PCBA Layer</b>	
L1 TOP	L4 Signal
L2 VCC	L5 GND
L3 Signal	L6 Bottom

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002 Block Diagram		
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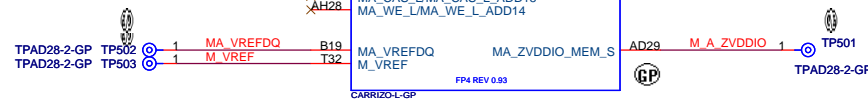
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CPU2A		1 OF 10	
		MEMORY A	
AE28	MA_ADD0	MA_DATA0	H17
X Y27	MA_ADD1	MA_DATA1	H17
X Y26	MA_ADD2	MA_DATA2	H20
X W28	MA_ADD3	MA_DATA3	E17
X W29	MA_ADD4	MA_DATA4	F17
X W26	MA_ADD5	MA_DATA5	K18
X U29	MA_ADD6	MA_DATA6	E20
X U25	MA_ADD7	MA_DATA7	
X U28	MA_ADD8		A21
X AG29	MA_ADD9	MA_DATA8	C21
X U27	MA_ADD10	MA_DATA9	C23
X T28	MA_ADD11	MA_DATA10	D23
X AK26	MA_ADD12	MA_DATA11	B20
X T28	MA_ADD13	MA_DATA12	B21
X T28	MA_ADD14/MA_BG1	MA_DATA13	B23
X T25	MA_ADD15/MA_ACT_L	MA_DATA14	A23
		MA_DATA15	
		MA_DATA16	G22
X AG26	MA_BANK0	MA_DATA17	H22
X AG27	MA_BANK1	MA_DATA18	E25
X T29	MA_BANK2/MA_BG0	MA_DATA19	G25
		MA_DATA20	J20
X E19	MA_DM0	MA_DATA21	E22
X D21	MA_DM1	MA_DATA22	H23
X K21	MA_DM2	MA_DATA23	J23
X F29	MA_DM3		
X AP28	MA_DM4	MA_DATA24	F26
X AV28	MA_DM5	MA_DATA25	E27
X AR22	MA_DM6	MA_DATA26	J26
X BC22	MA_DM7	MA_DATA27	J27
X K29	MA_DM8	MA_DATA28	H25
		MA_DATA29	E26
X H19	MA_DQS_H0	MA_DATA30	G28
X G19	MA_DQS_L0	MA_DATA31	G29
X B22	MA_DQS_H1		
X A22	MA_DQS_L1	MA_DATA32	AN26
X F23	MA_DQS_H2	MA_DATA33	AP29
X E23	MA_DQS_L2	MA_DATA34	AR26
X G27	MA_DQS_H3	MA_DATA35	AP24
X F27	MA_DQS_L3	MA_DATA36	AN29
X AP25	MA_DQS_H4	MA_DATA37	AN27
X AP26	MA_DQS_L4	MA_DATA38	AR29
X W27	MA_DQS_H5	MA_DATA39	AR27
X AV27	MA_DQS_L5		
X V22	MA_DQS_H6	MA_DATA40	AU26
X AU22	MA_DQS_L6	MA_DATA41	AV29
X BA21	MA_DQS_H7	MA_DATA42	AU23
X AY21	MA_DQS_L7	MA_DATA43	AW29
X L27	MA_DQS_H8	MA_DATA44	AU29
X L26	MA_DQS_L8	MA_DATA45	AU28
		MA_DATA46	AW26
X AE25	MA_CLK_H0	MA_DATA47	AT25
X AE26	MA_CLK_L0		
X AD27	MA_CLK_H1	MA_DATA48	AV23
X AB28	MA_CLK_L1	MA_DATA49	AW23
X AB29	MA_CLK_H2	MA_DATA50	AV20
X AB25	MA_CLK_L2	MA_DATA51	AW20
X AB26	MA_CLK_H3	MA_DATA52	AR23
	MA_CLK_L3	MA_DATA53	AT23
		MA_DATA54	AR29
X N29	MA_RESET_L	MA_DATA55	AT20
X AE29	MA_EVENT_L		
		MA_DATA56	BB23
X P27	MA_CKE0	MA_DATA57	BB22
X P29	MA_CKE1	MA_DATA58	BB20
		MA_DATA59	AY19
		MA_DATA60	BA23
X AK27	MA0_ODT0	MA_DATA61	BC23
X AL26	MA0_ODT1	MA_DATA62	BC21
X AH25	MA1_ODT0	MA_DATA63	
X AL25	MA1_ODT1		
		MA_CHECK0	K26
X AH26	MA0_CS_L0	MA_CHECK1	N26
X AL29	MA0_CS_L1	MA_CHECK2	N28
X AH29	MA1_CS_L0	MA_CHECK3	J29
X AL28	MA1_CS_L1	MA_CHECK4	K25
		MA_CHECK5	L29
		MA_CHECK6	N25
		MA_CHECK7	
X AG24	MA_RAS_L/MA_RAS_L_ADD16		
X AK29	MA_CAS_L/MA_CAS_L_ADD15		
X AH28	MA_WE_L/MA_WE_L_ADD14		

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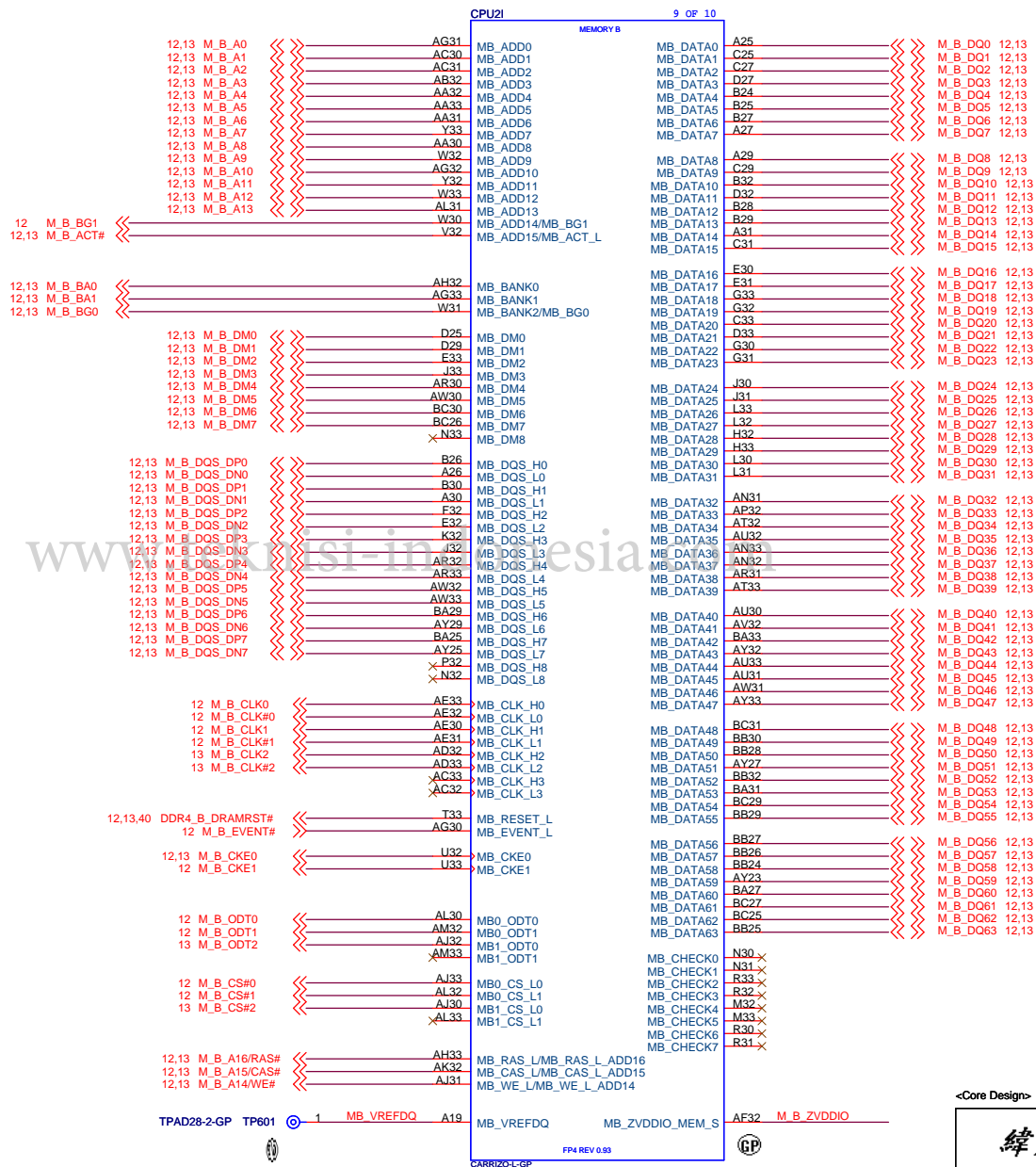
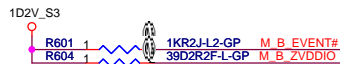
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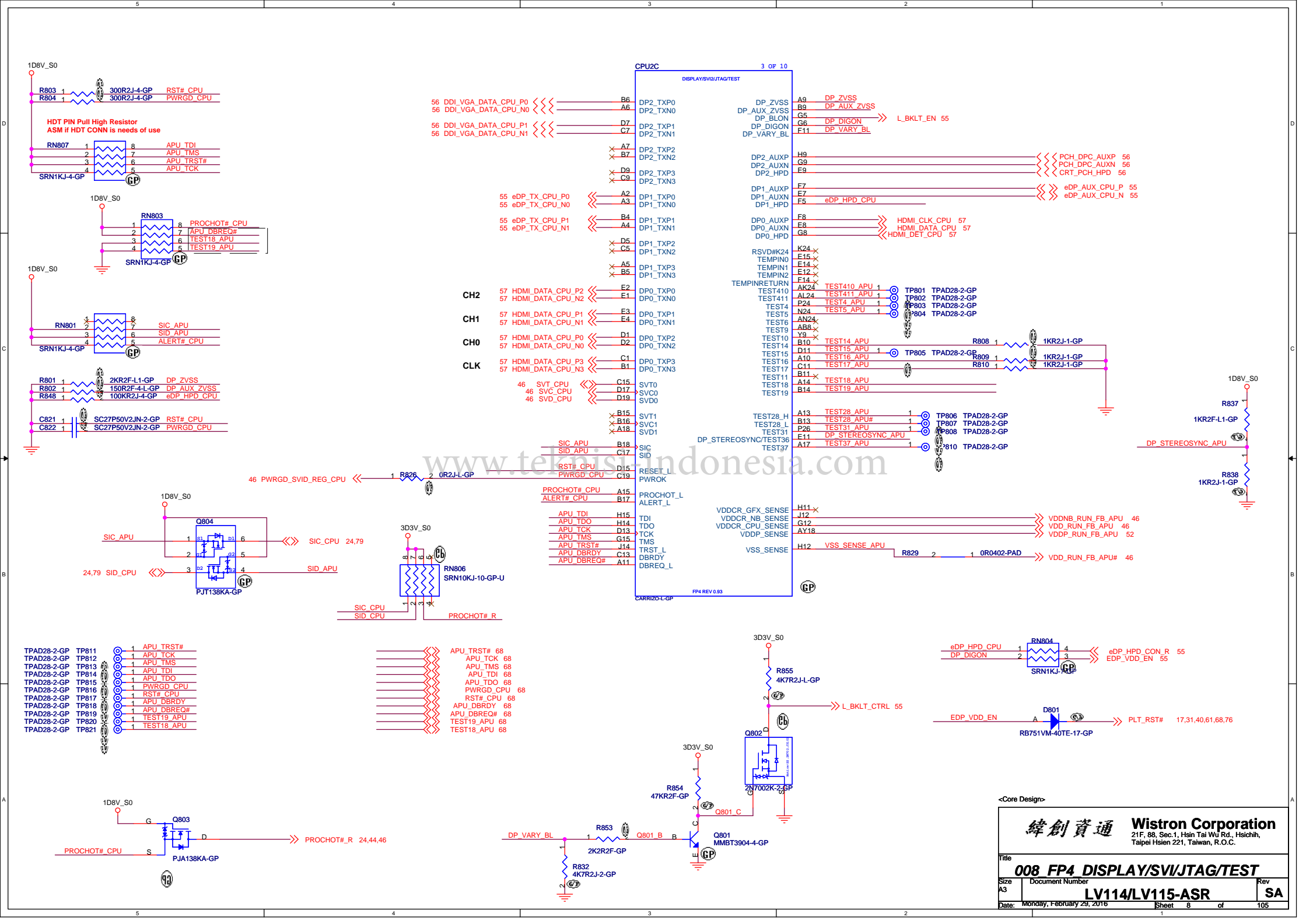
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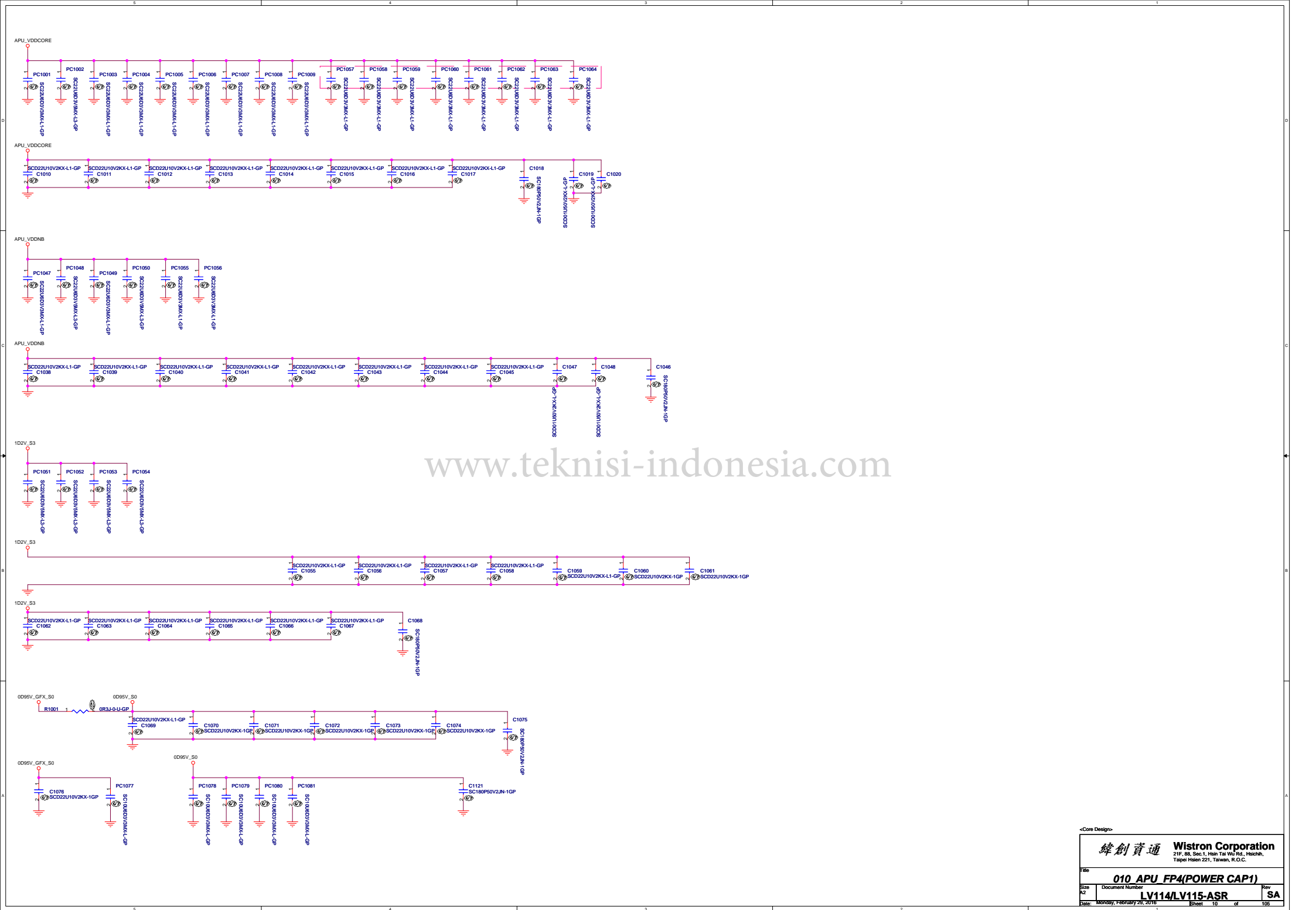




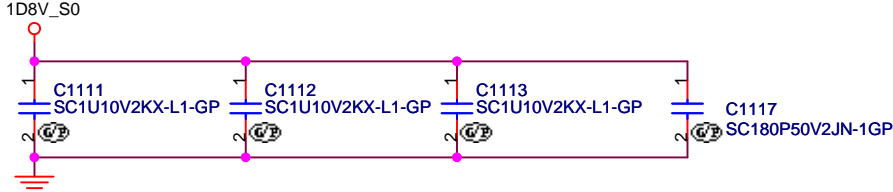
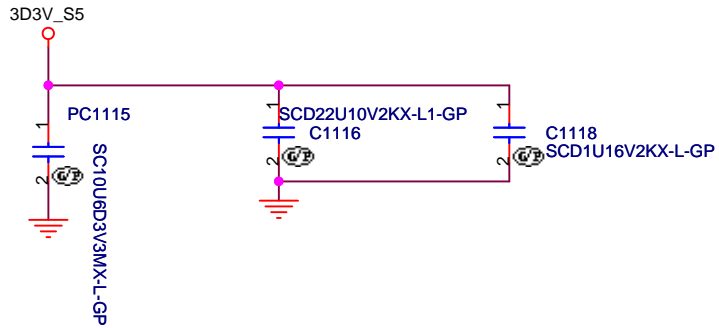
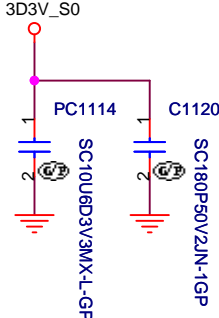
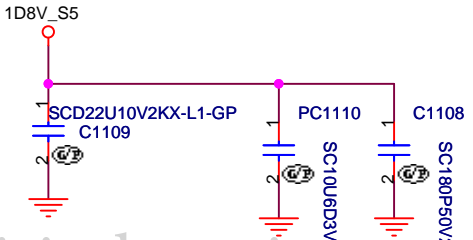
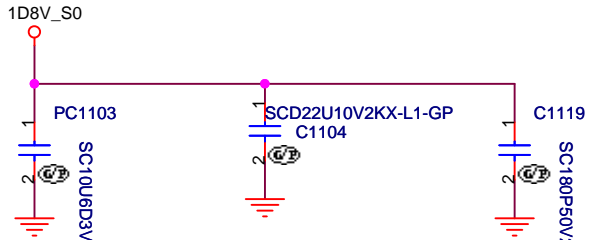
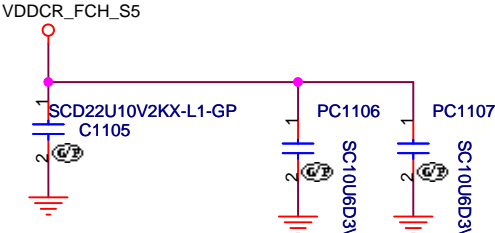
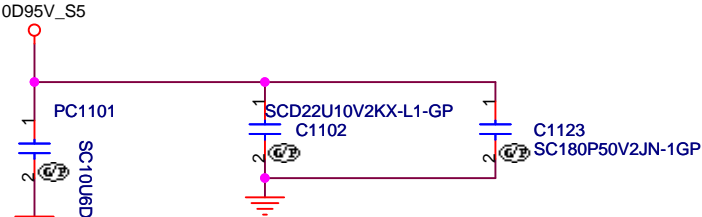








APU Caps



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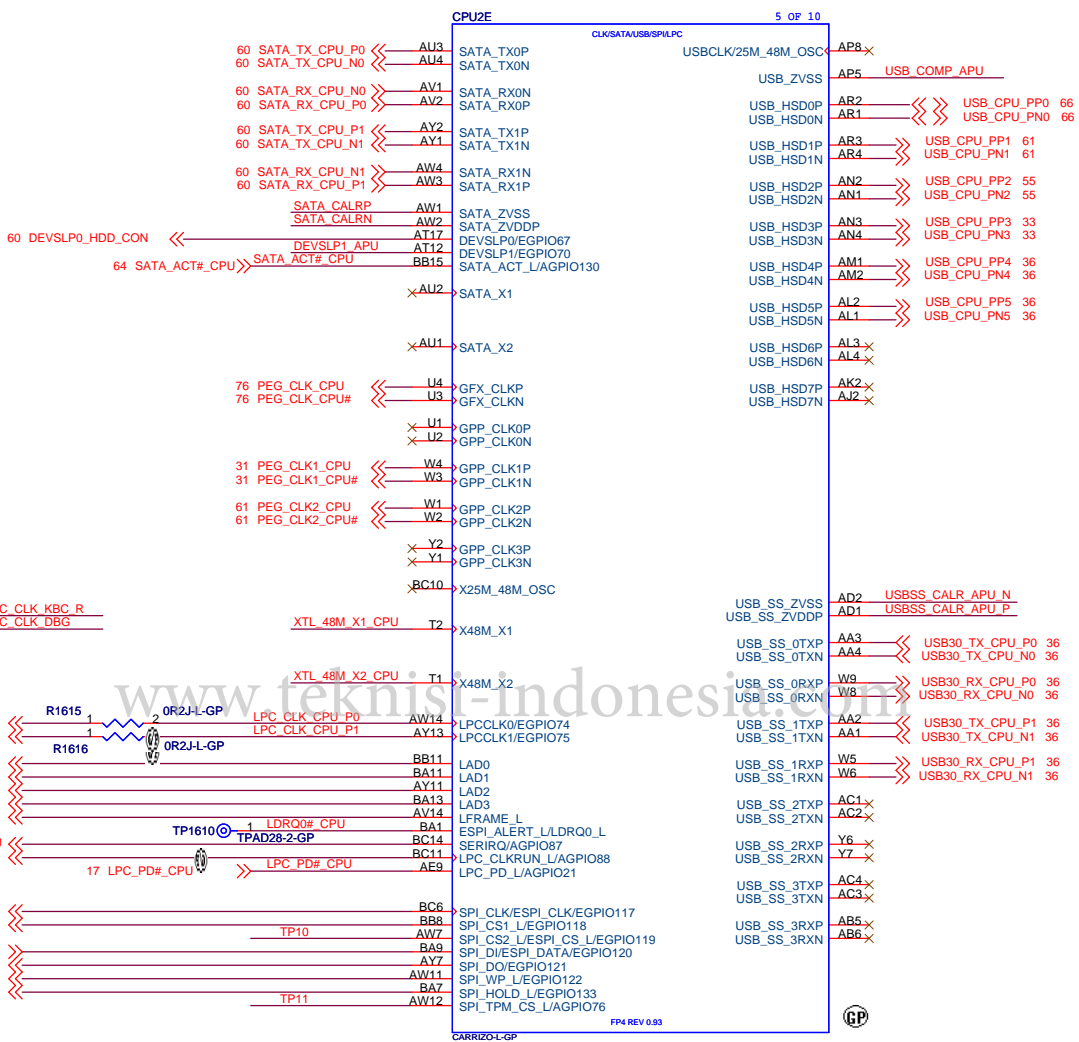
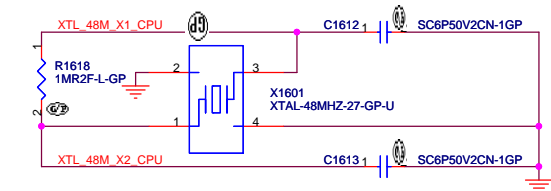
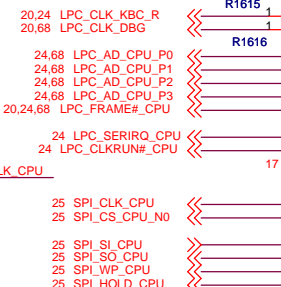
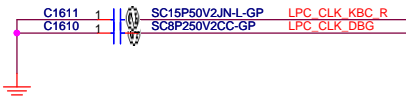
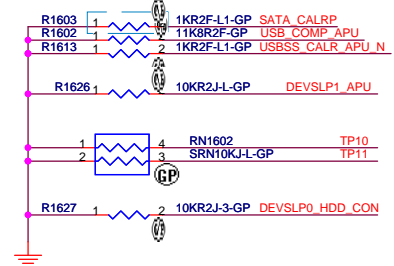
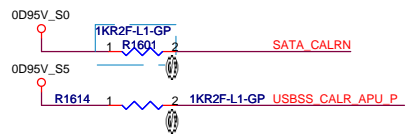
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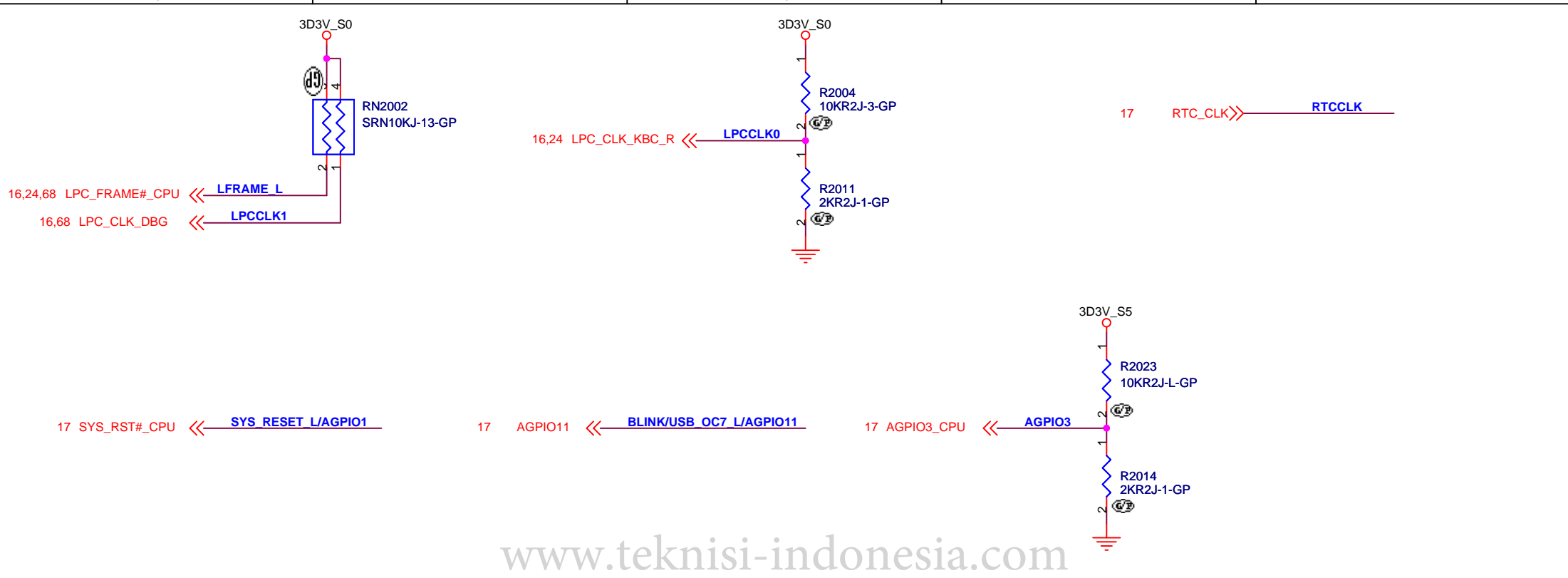
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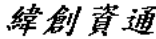
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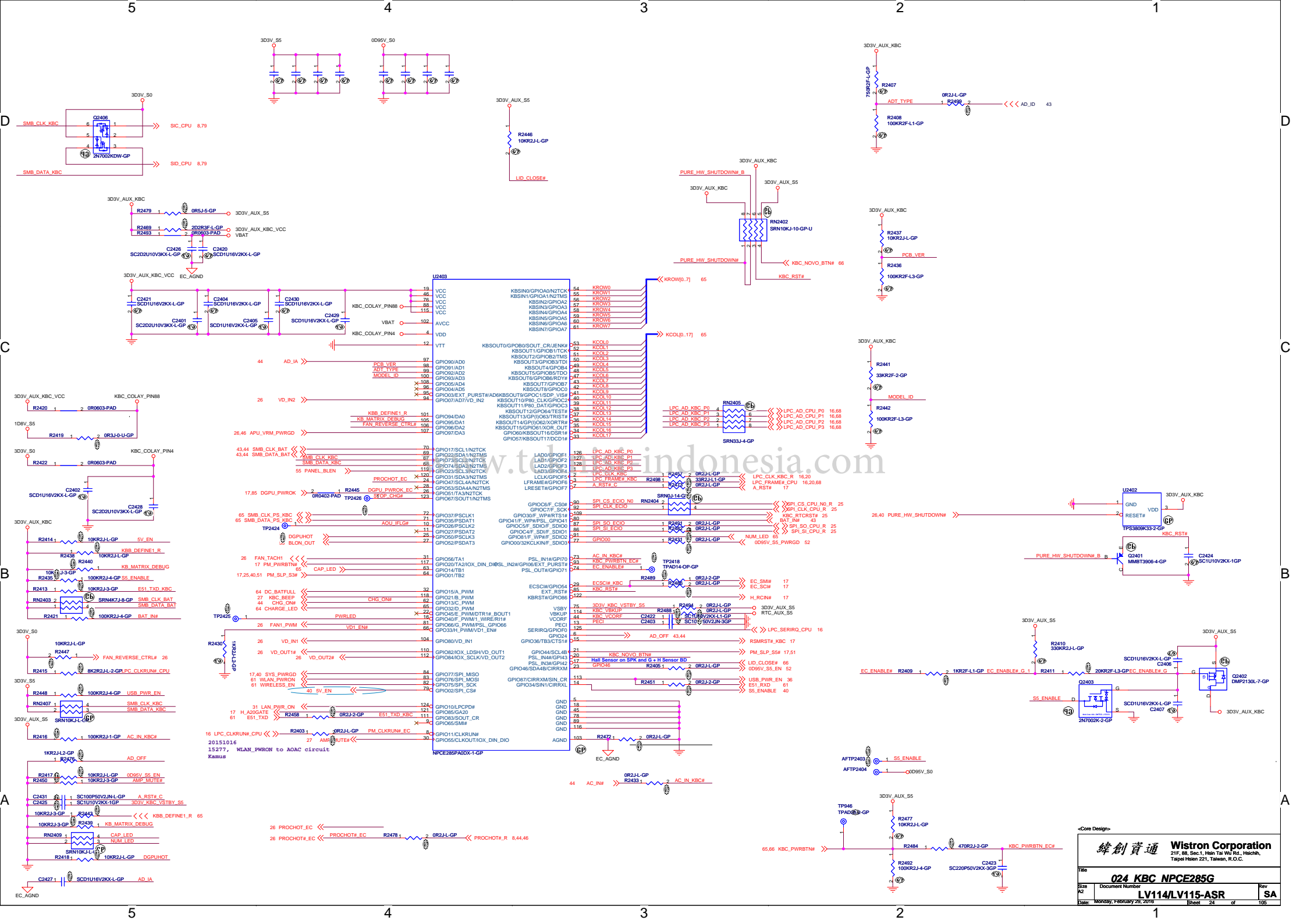
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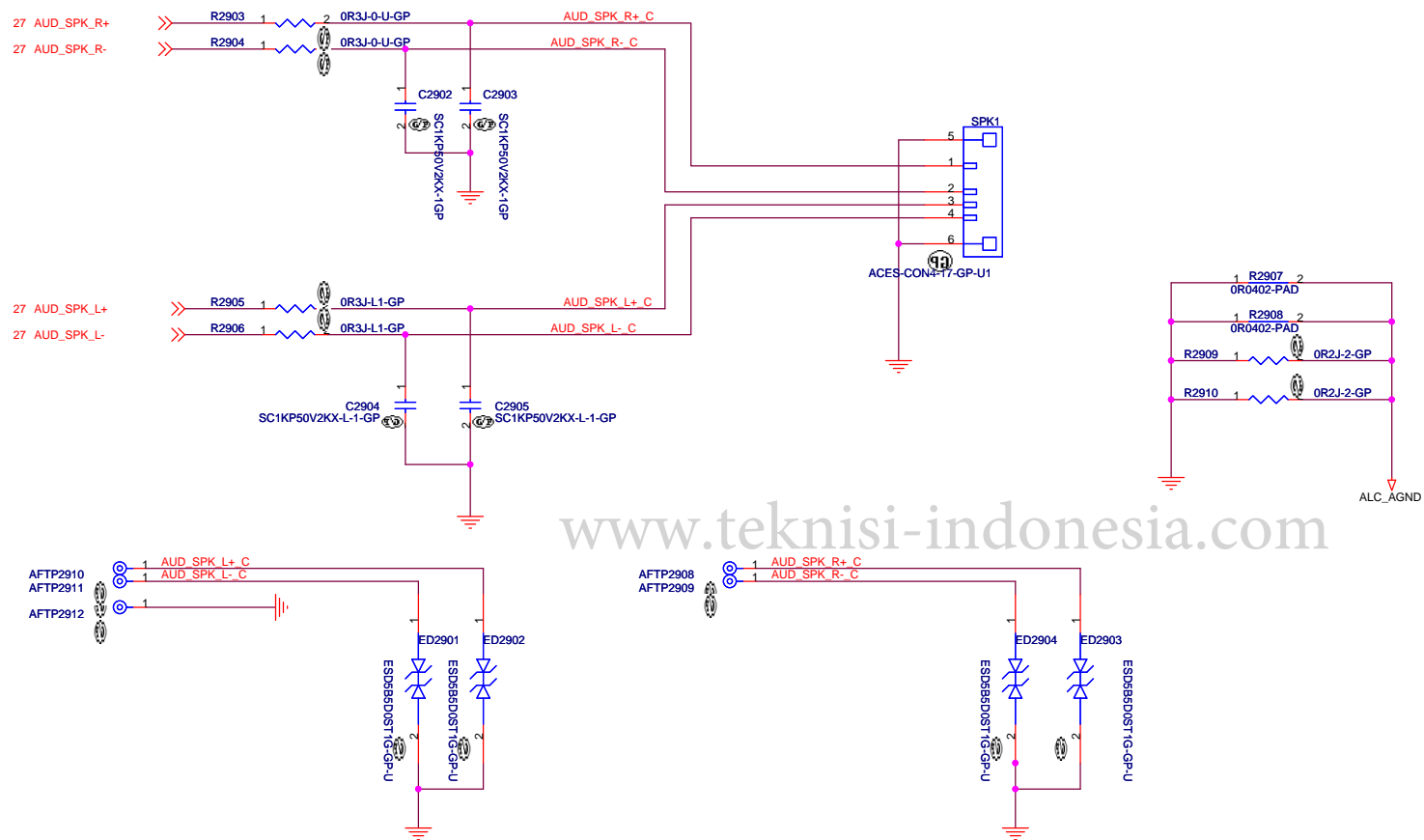








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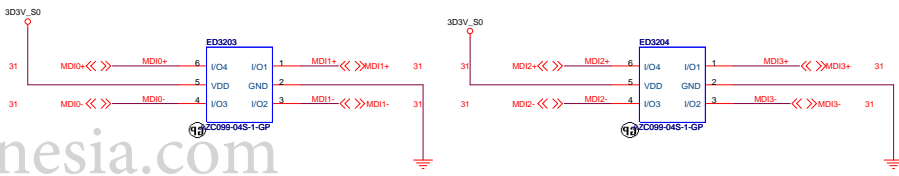
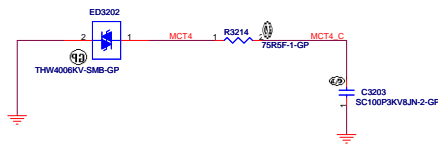
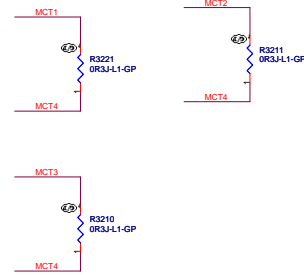
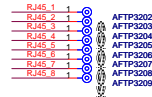
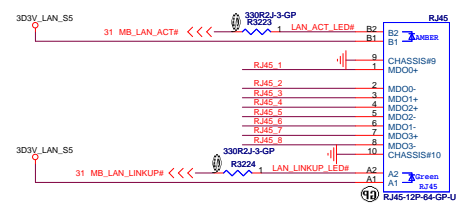
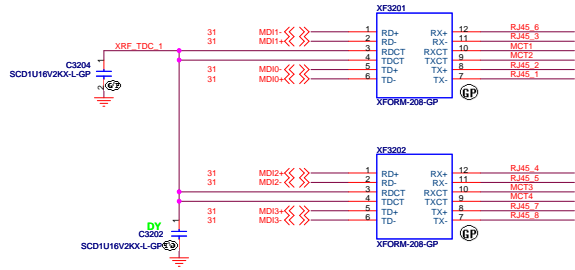
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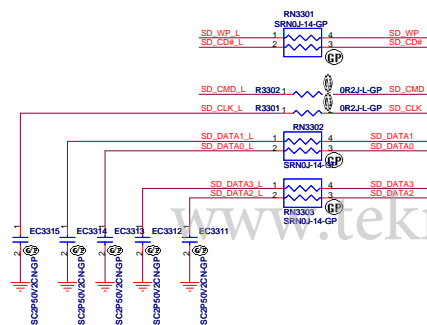
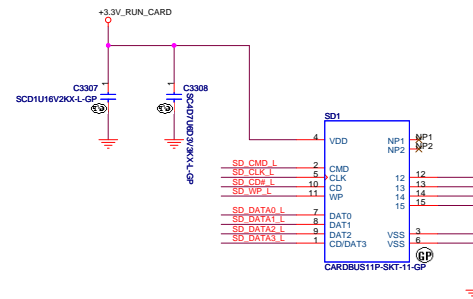
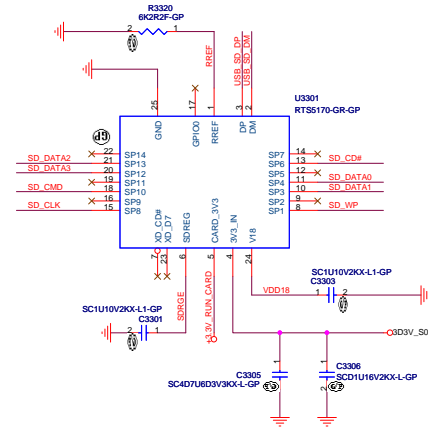
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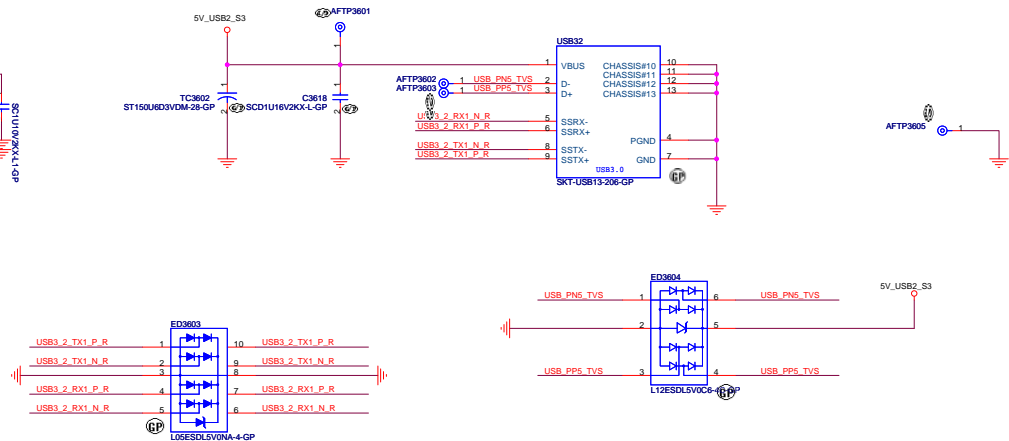
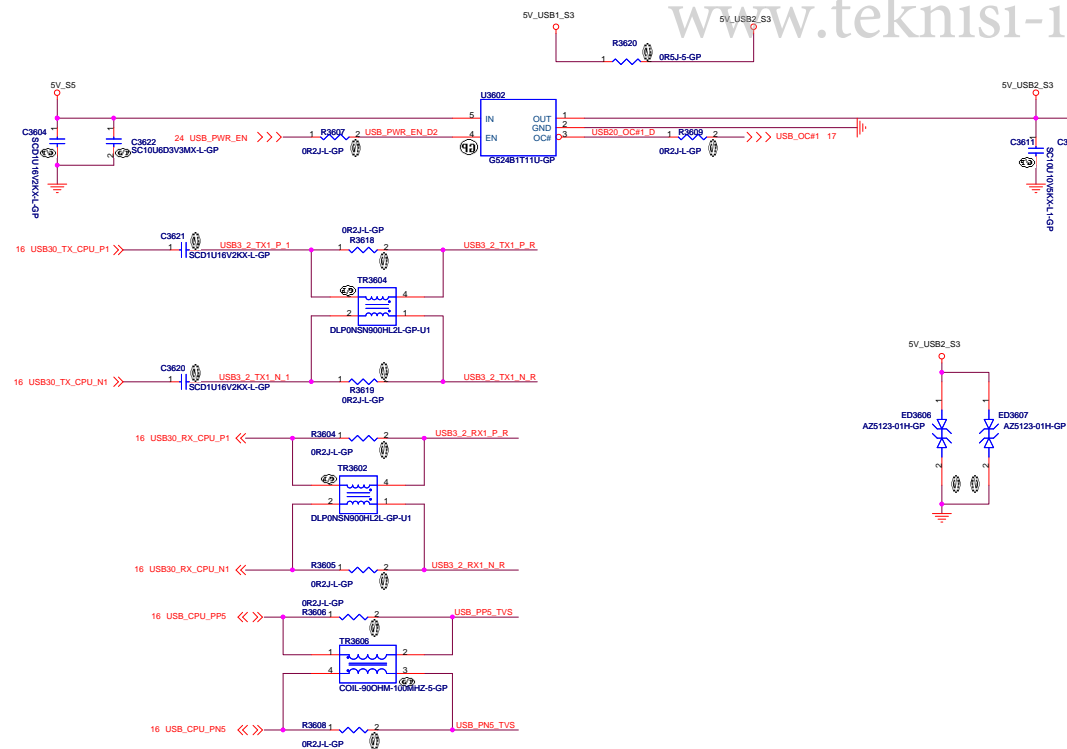
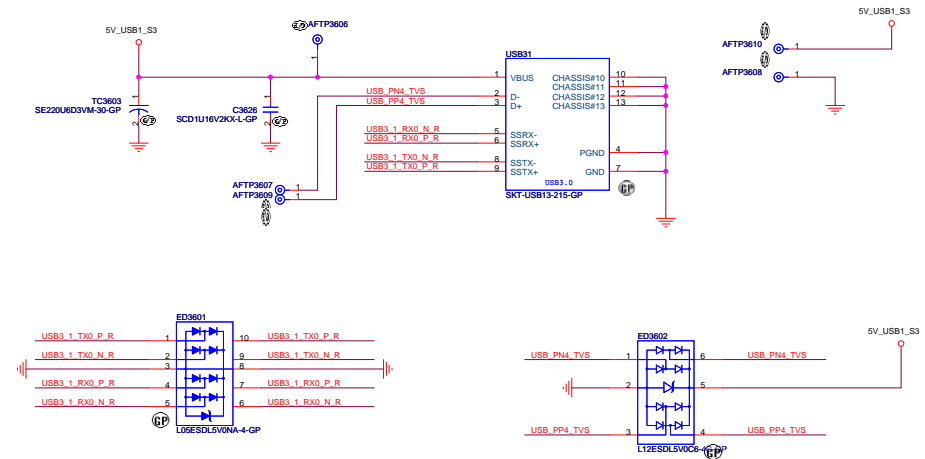
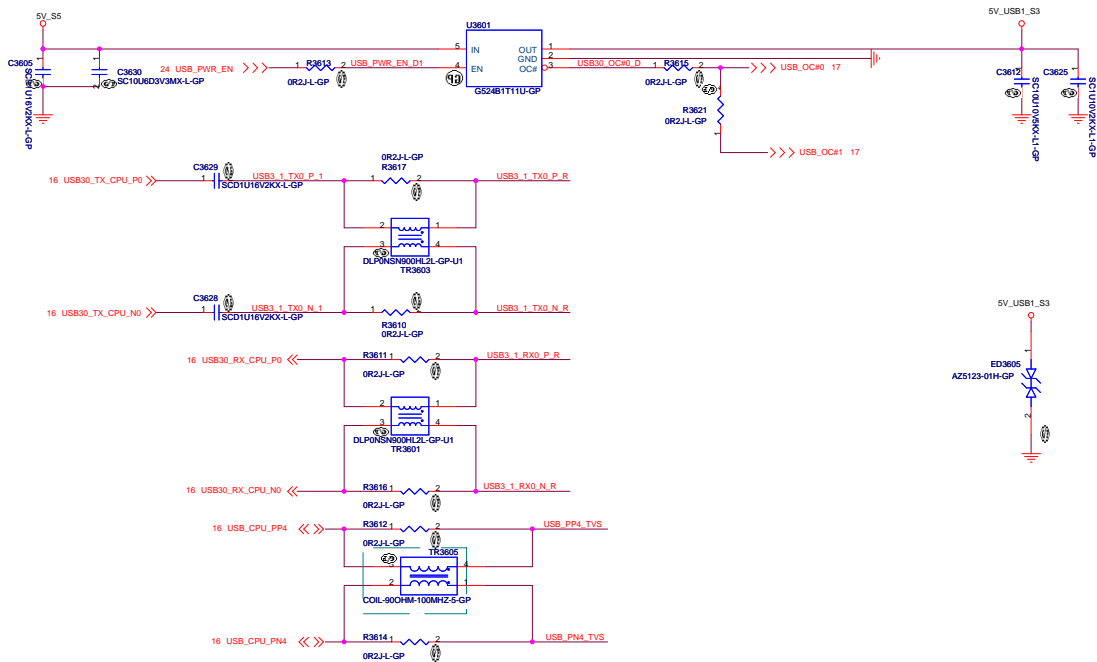
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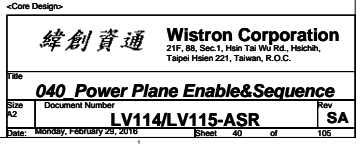
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Title		
039 (Reserved)		
Size	Document Number	Rev
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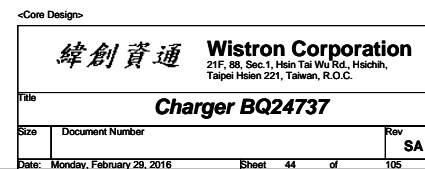
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Size	Document Number	Rev
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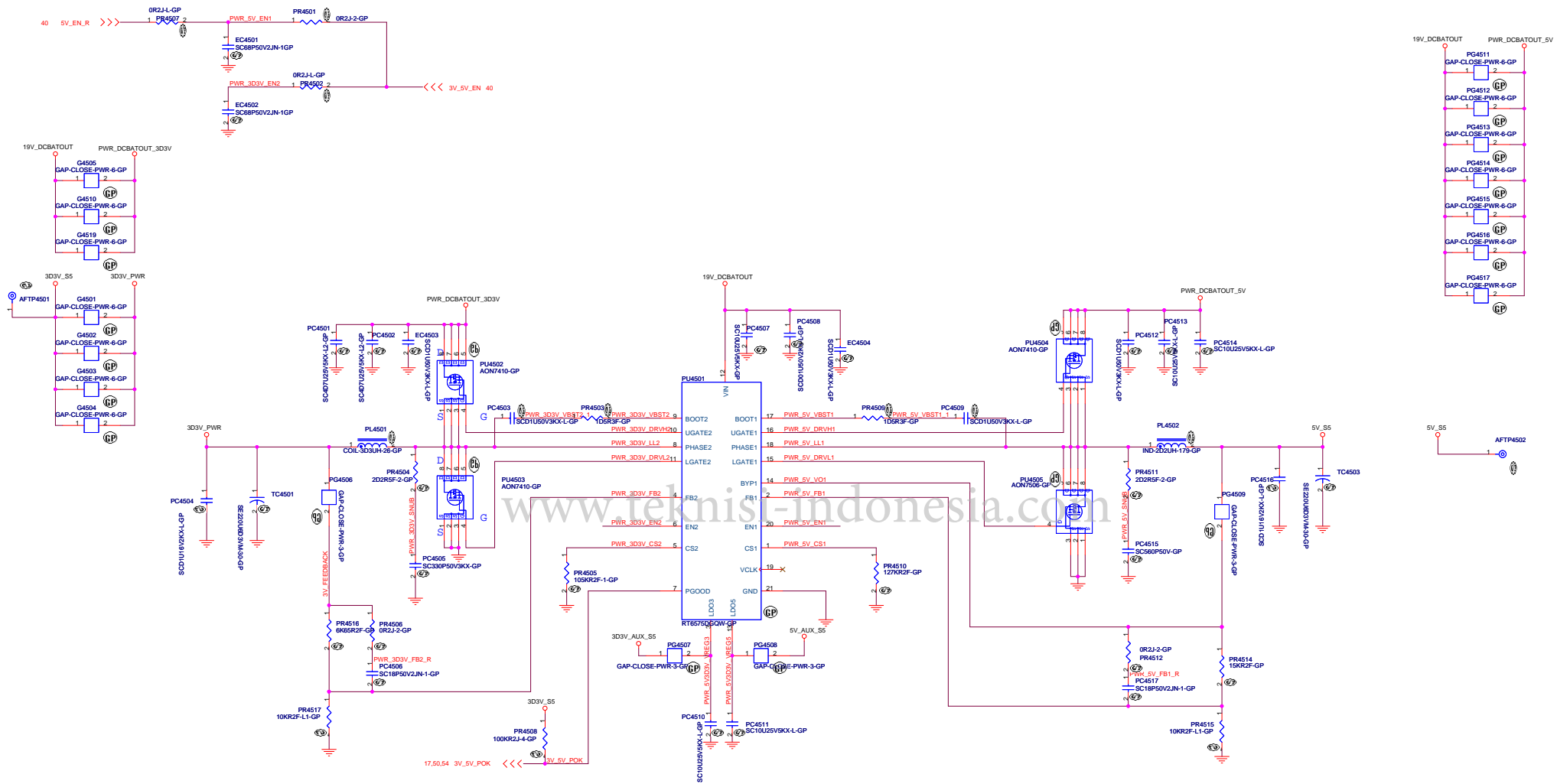
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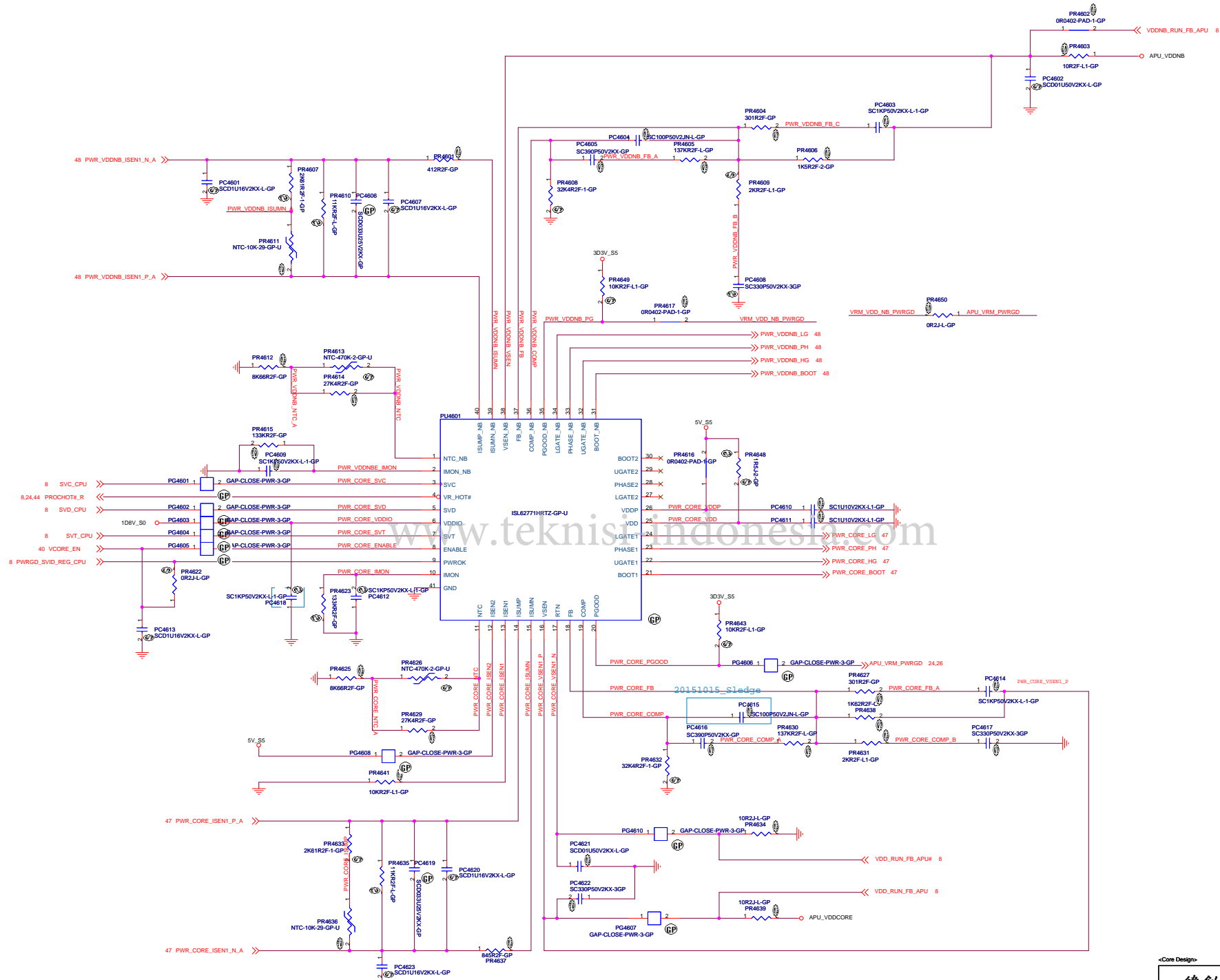




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Size	Document Number	Rev	SA
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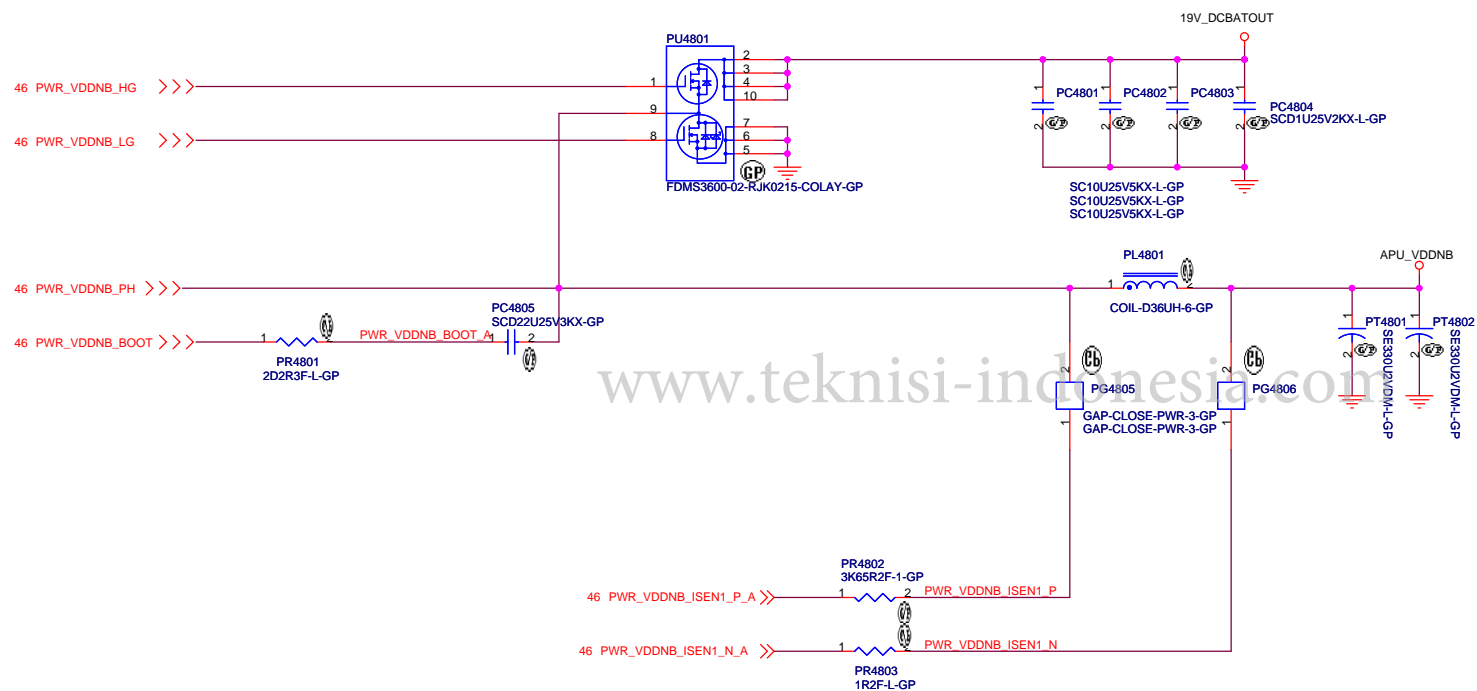


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Taipei Hsien 221, Taiwan, R.O.C.

046 ISL62771 CPUCORE			
File	Document Number	Rev	SA
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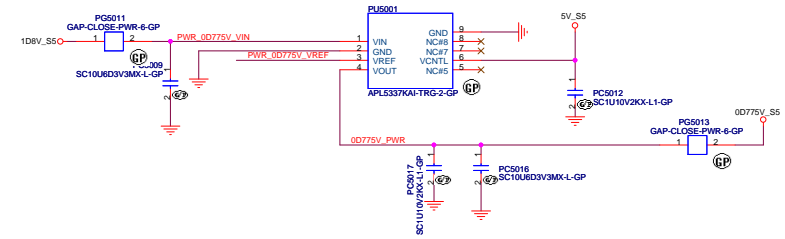
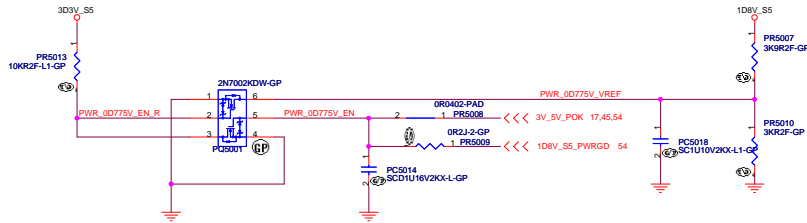
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Title			
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Size	Document Number	Rev
A2	LV114/LV115-ASR	SA
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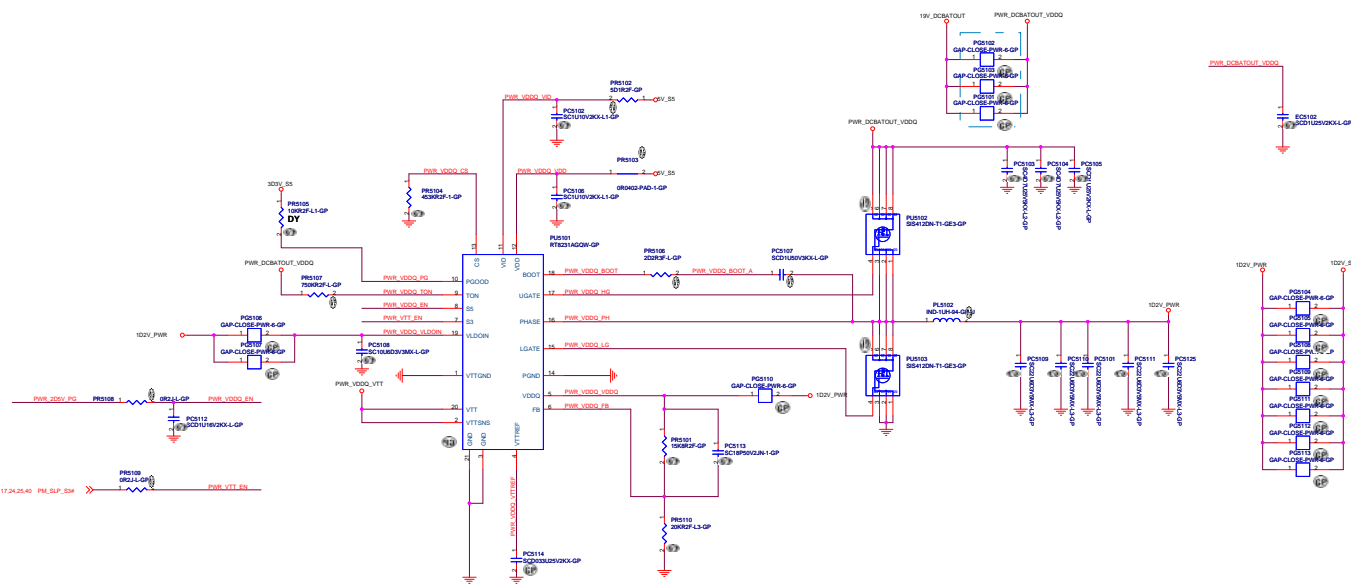


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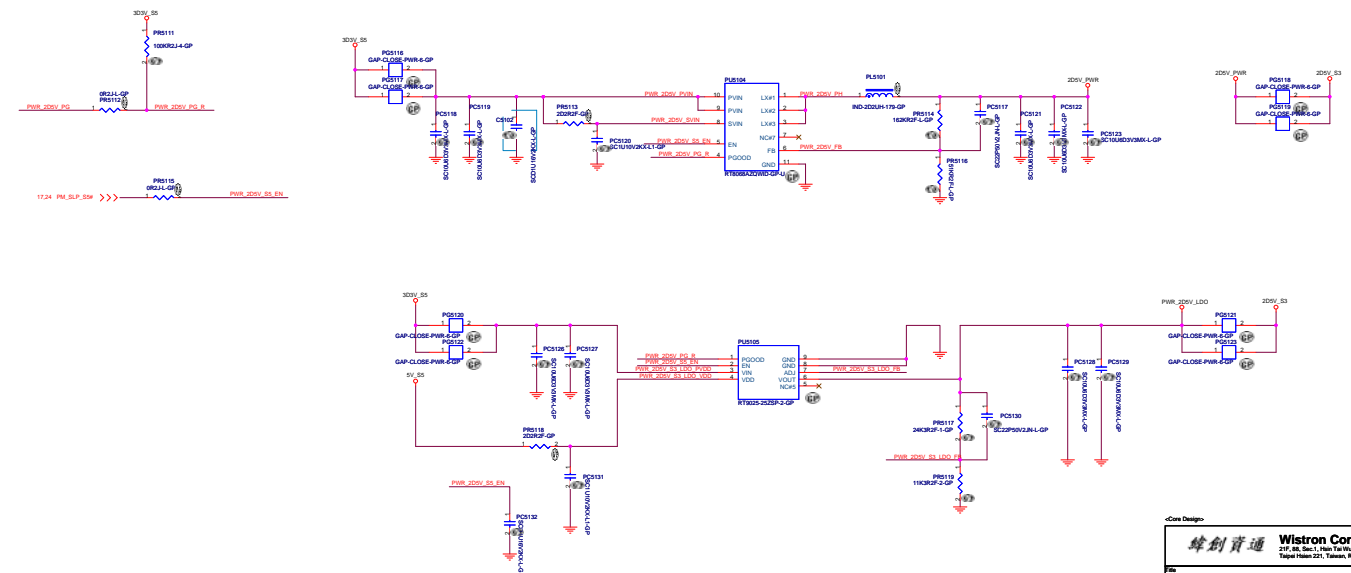
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Title

**050 12V RT8509**

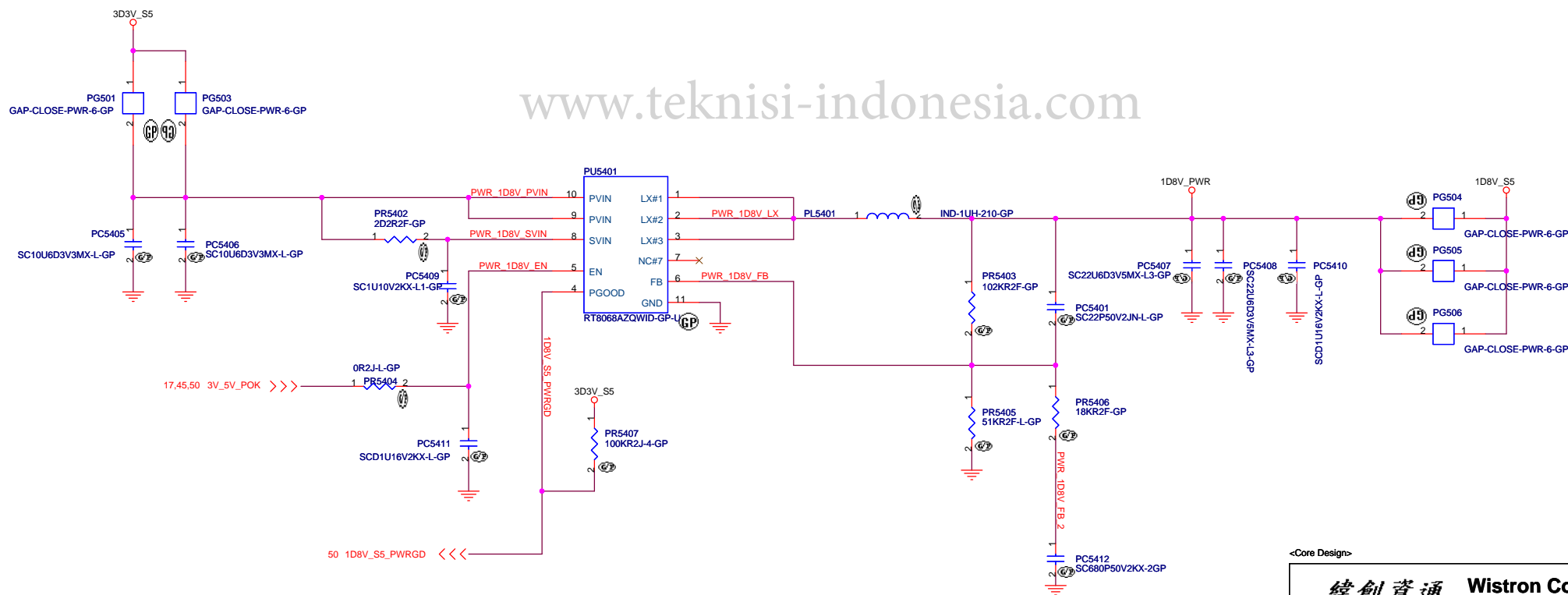
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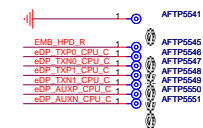
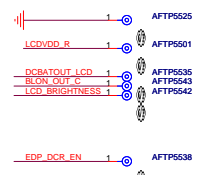
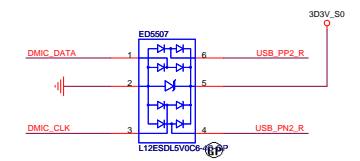
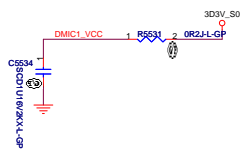
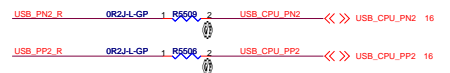
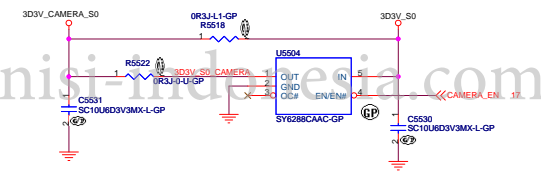
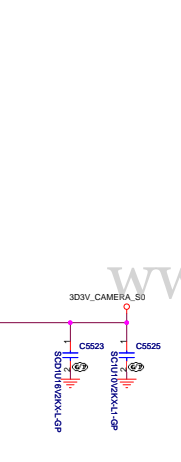
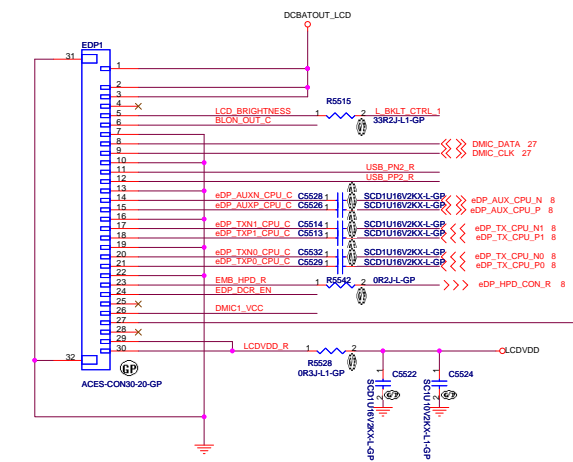
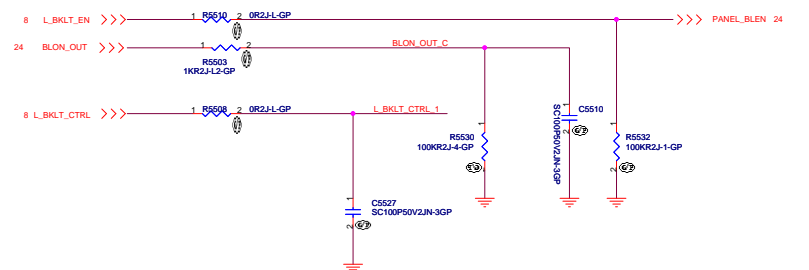
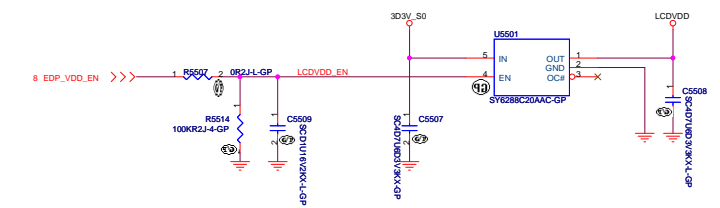
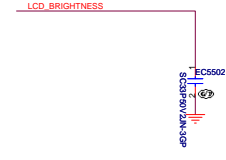
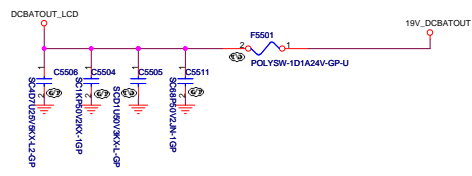
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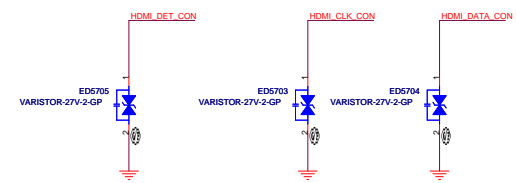
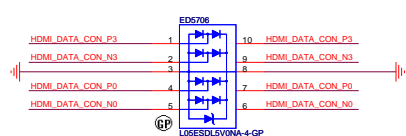
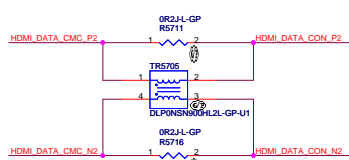
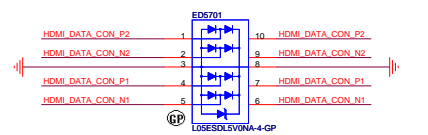
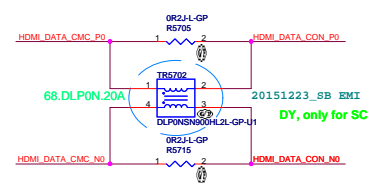
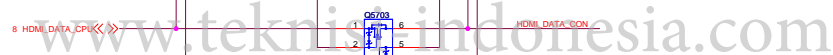
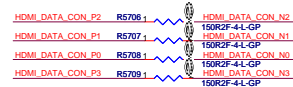
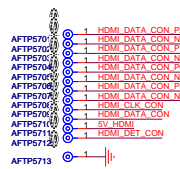
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058 Display Port(Reserved)

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Title

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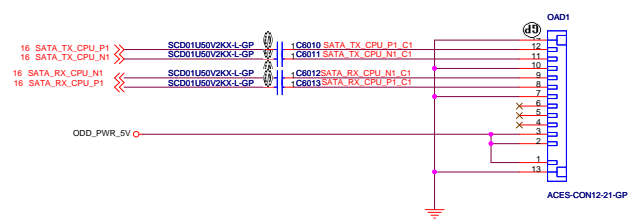
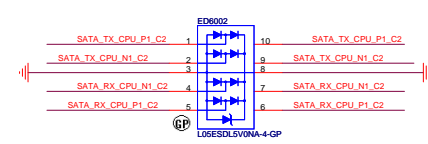
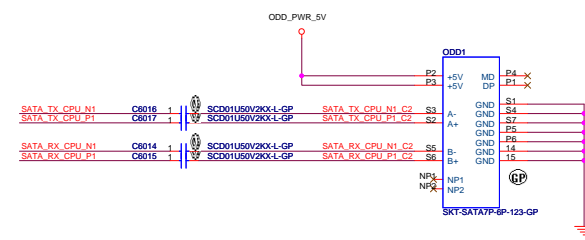
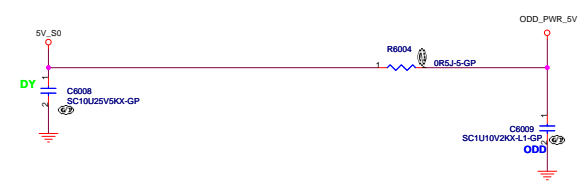
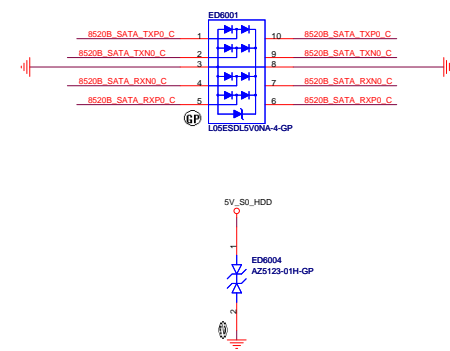
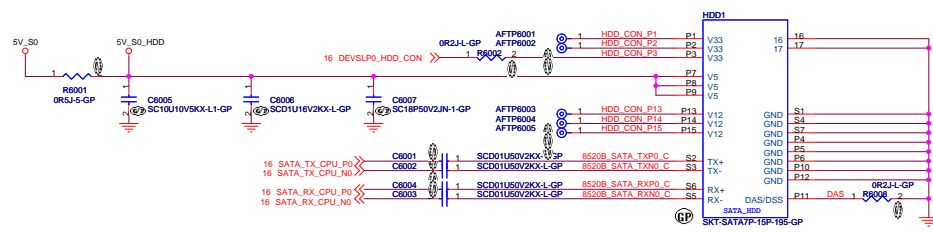
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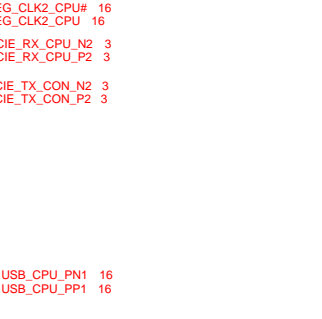
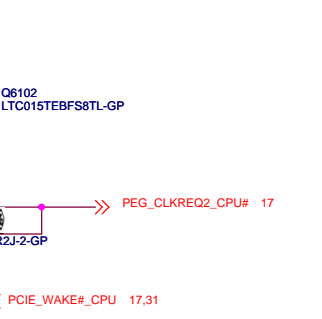
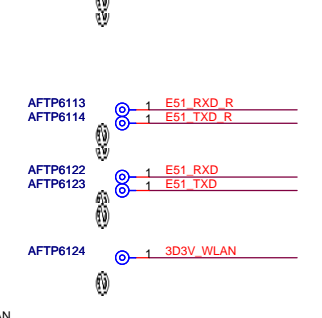
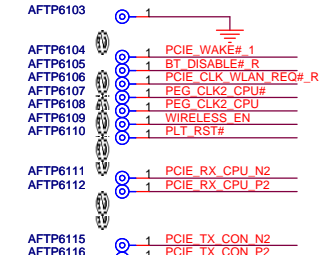
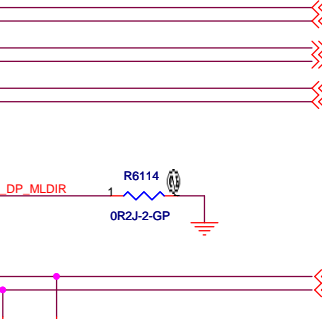
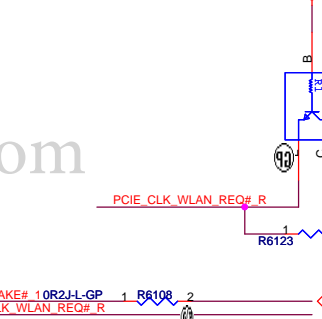
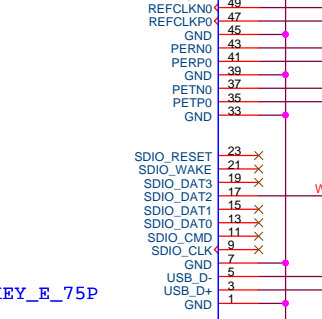
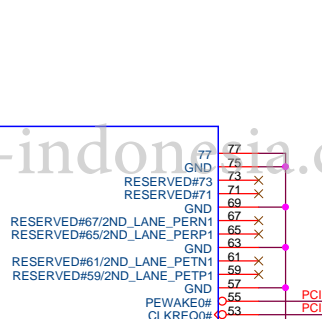
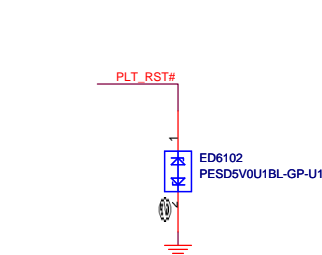
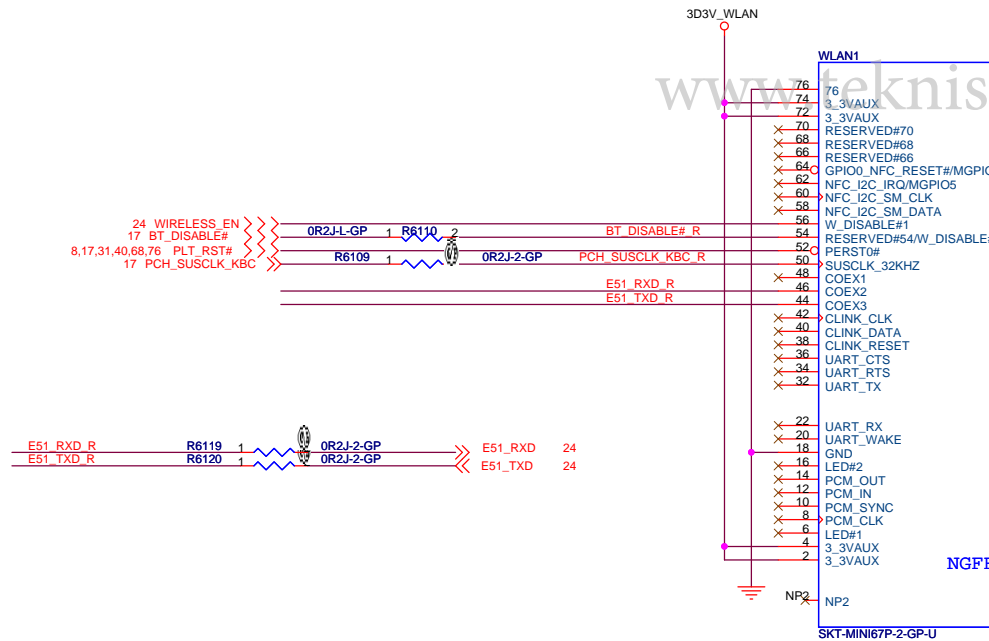
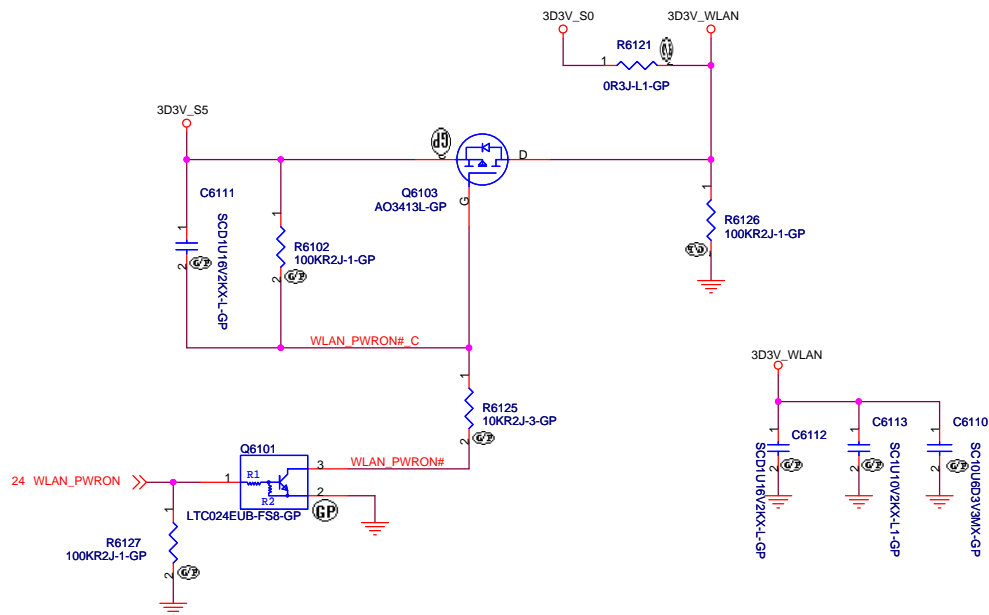
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Title: **NGFF WLAN CONN**

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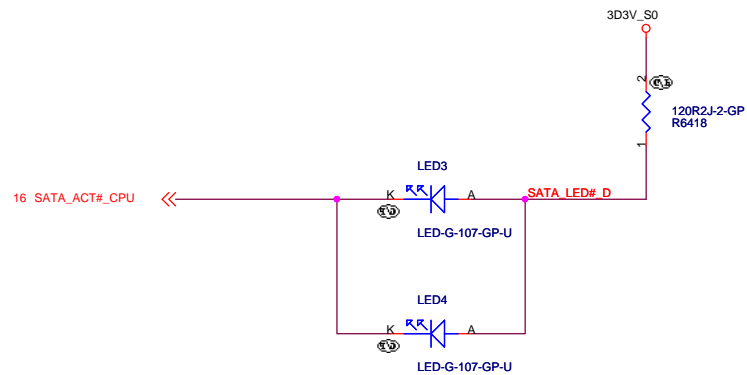
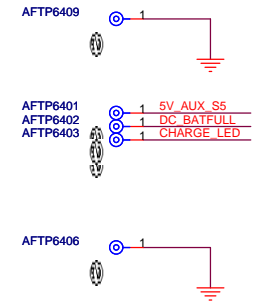
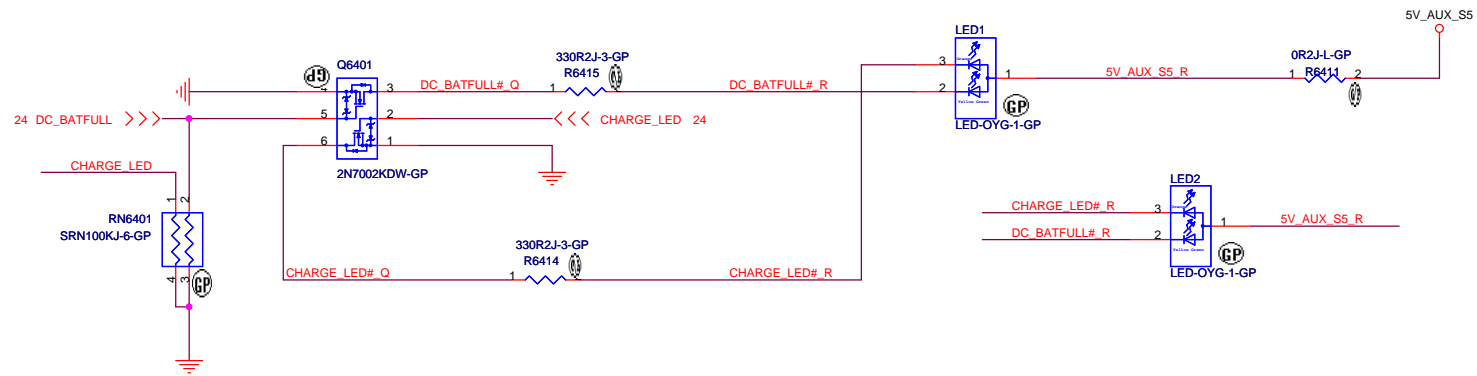
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Title			
<b>062 WWAN CONN(Reserved)</b>			
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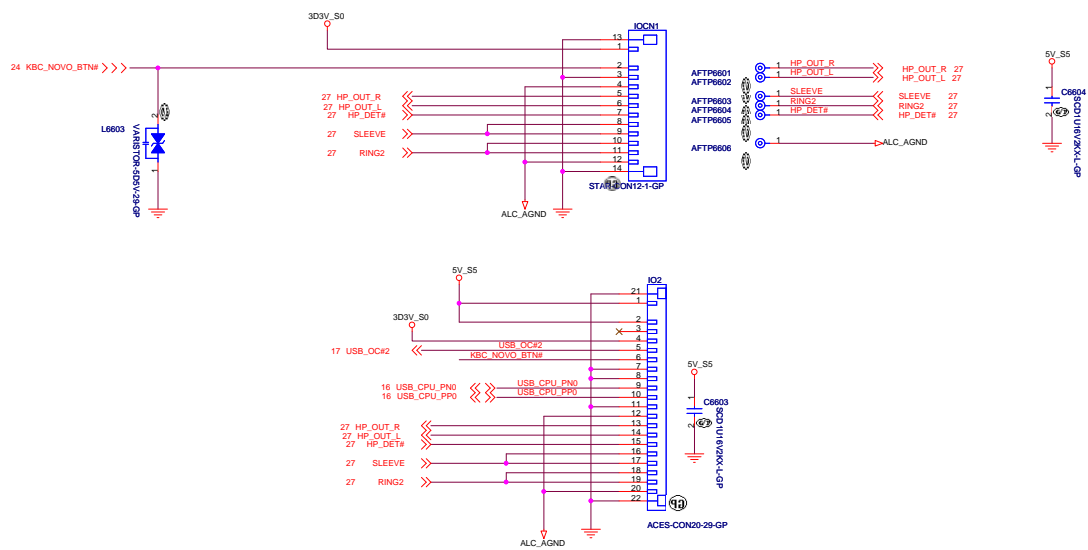
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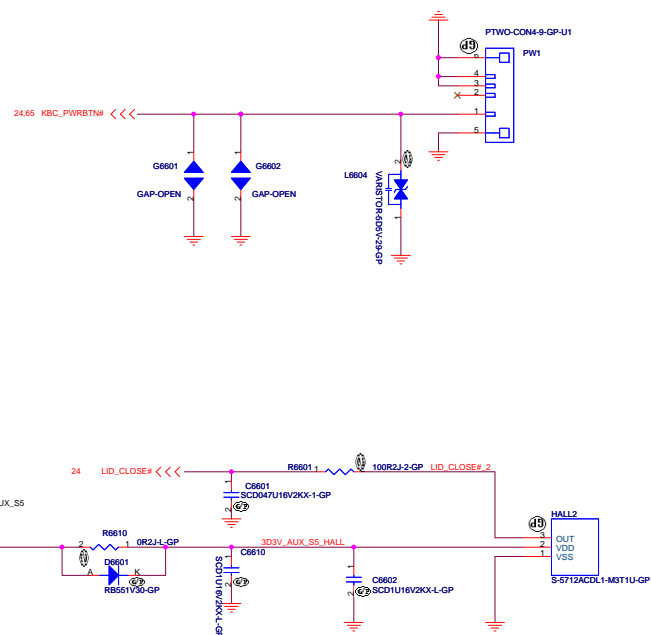
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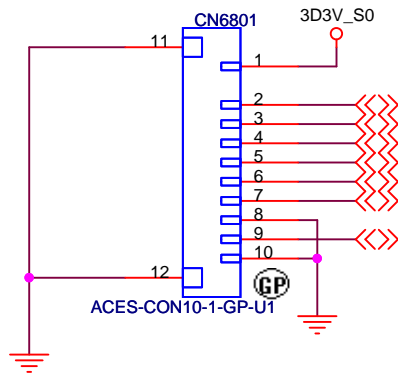
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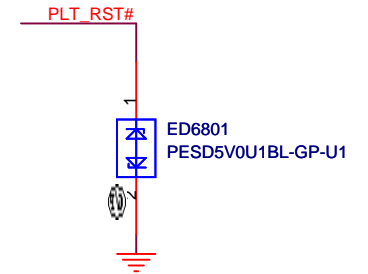
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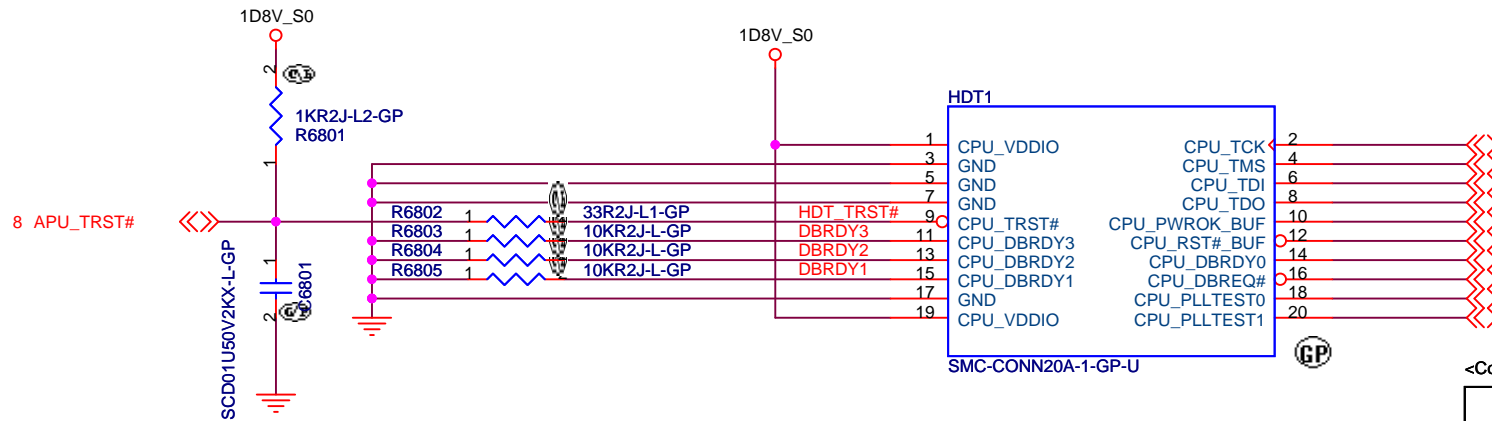
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067 Hall Sensor		
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LPC\_AD\_CPU\_P1 16,24  
LPC\_AD\_CPU\_P2 16,24  
LPC\_AD\_CPU\_P3 16,24  
LPC\_FRAME#\_CPU 16,20,24  
PLT\_RST# 8,17,31,40,61,76  
LPC\_CLK\_DBG 16,20



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APU\_TCK 8  
APU\_TMS 8  
APU\_TDI 8  
APU\_TDO 8  
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RST#\_CPU 8  
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068 HDT/Debug port		
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Title		
069 SENSOR HUB(Reserved)		
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070 G Sensor		
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Title

071\_Thunderbolt (1/5)(Reserved)

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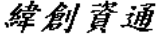
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073 Thunderbolt (3/5)(Reserved)

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**074 Thunderbolt (4/5)(Reserved)**

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Title

075 Thunderbolt (5/5)(Reserved)

Size

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Document Number

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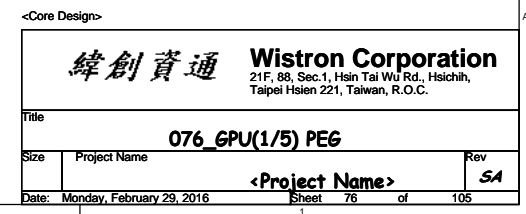
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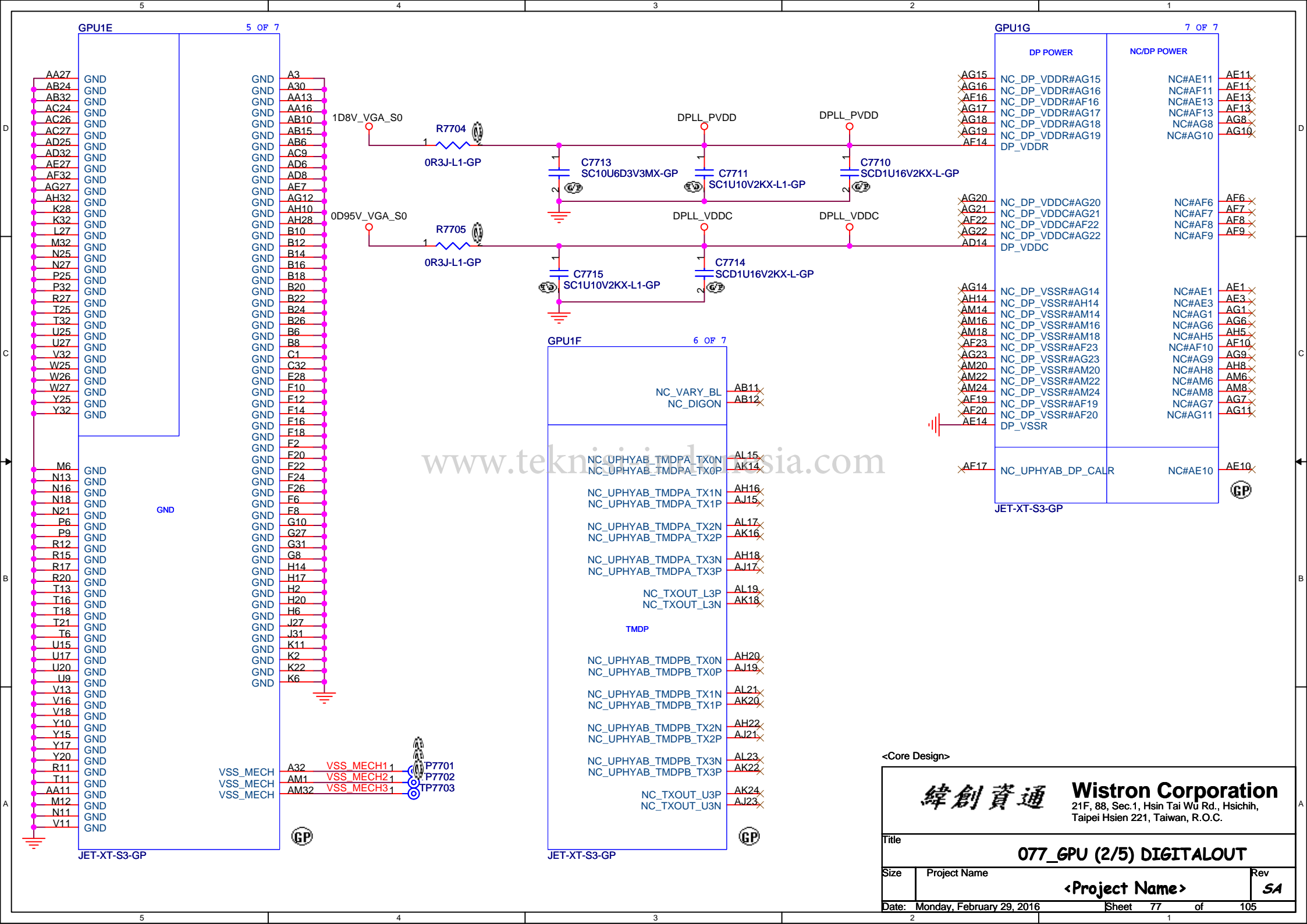
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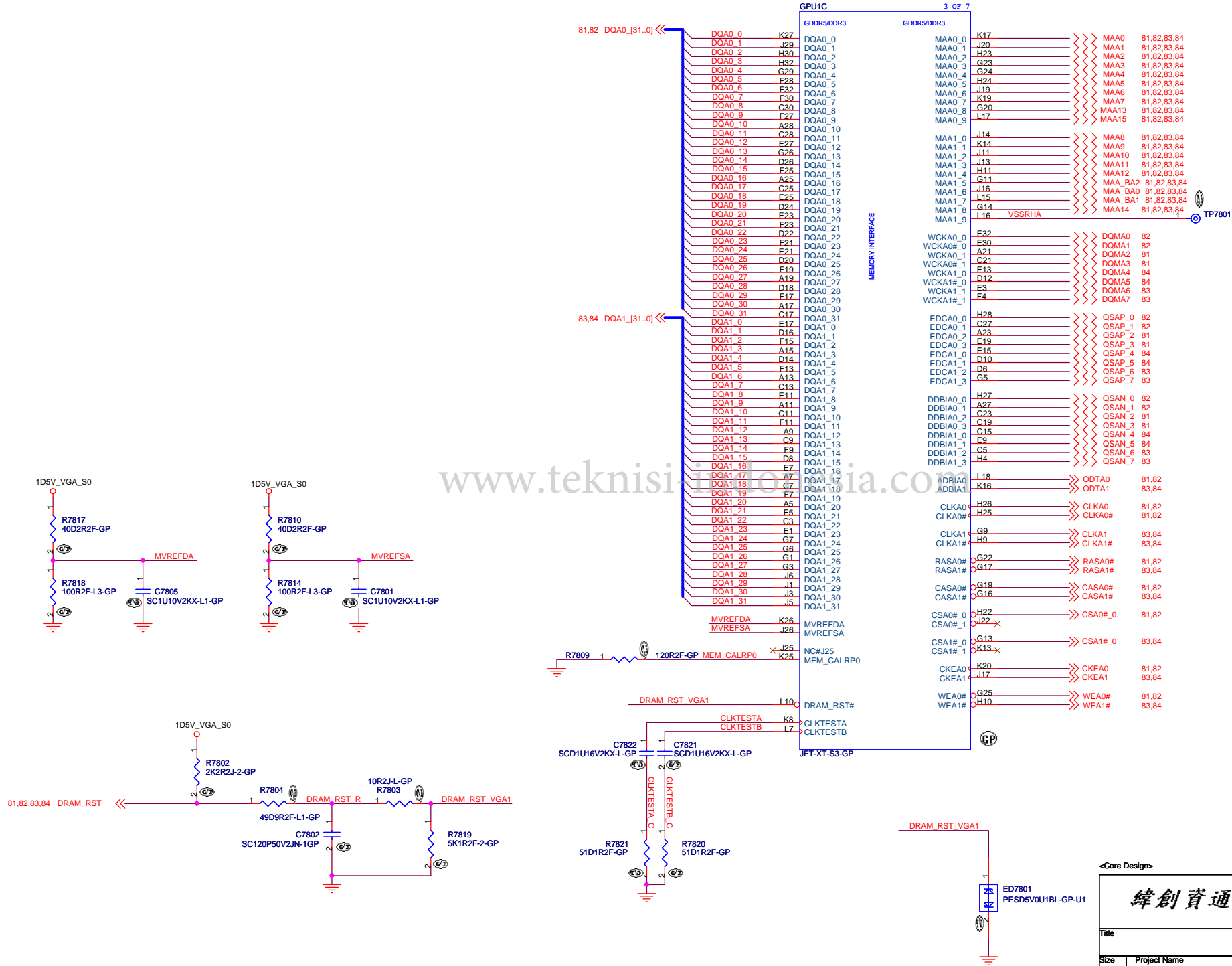
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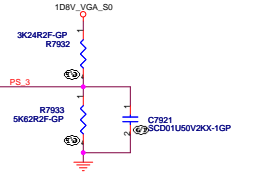
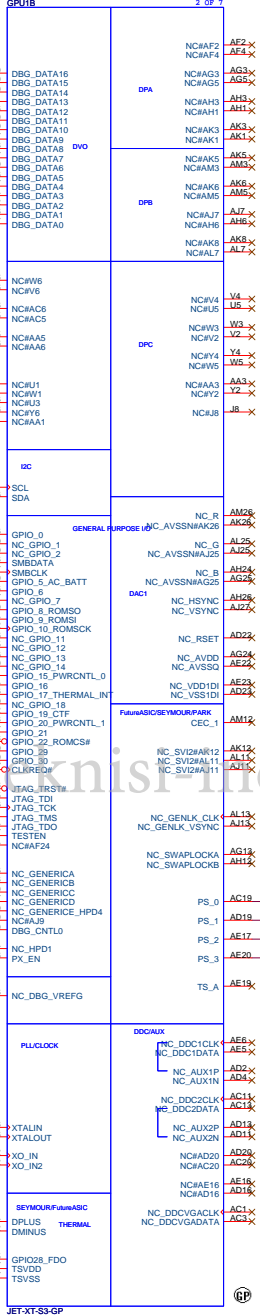
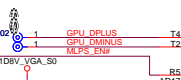
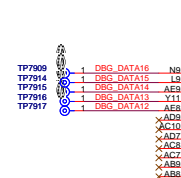
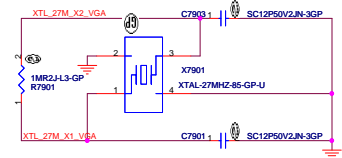
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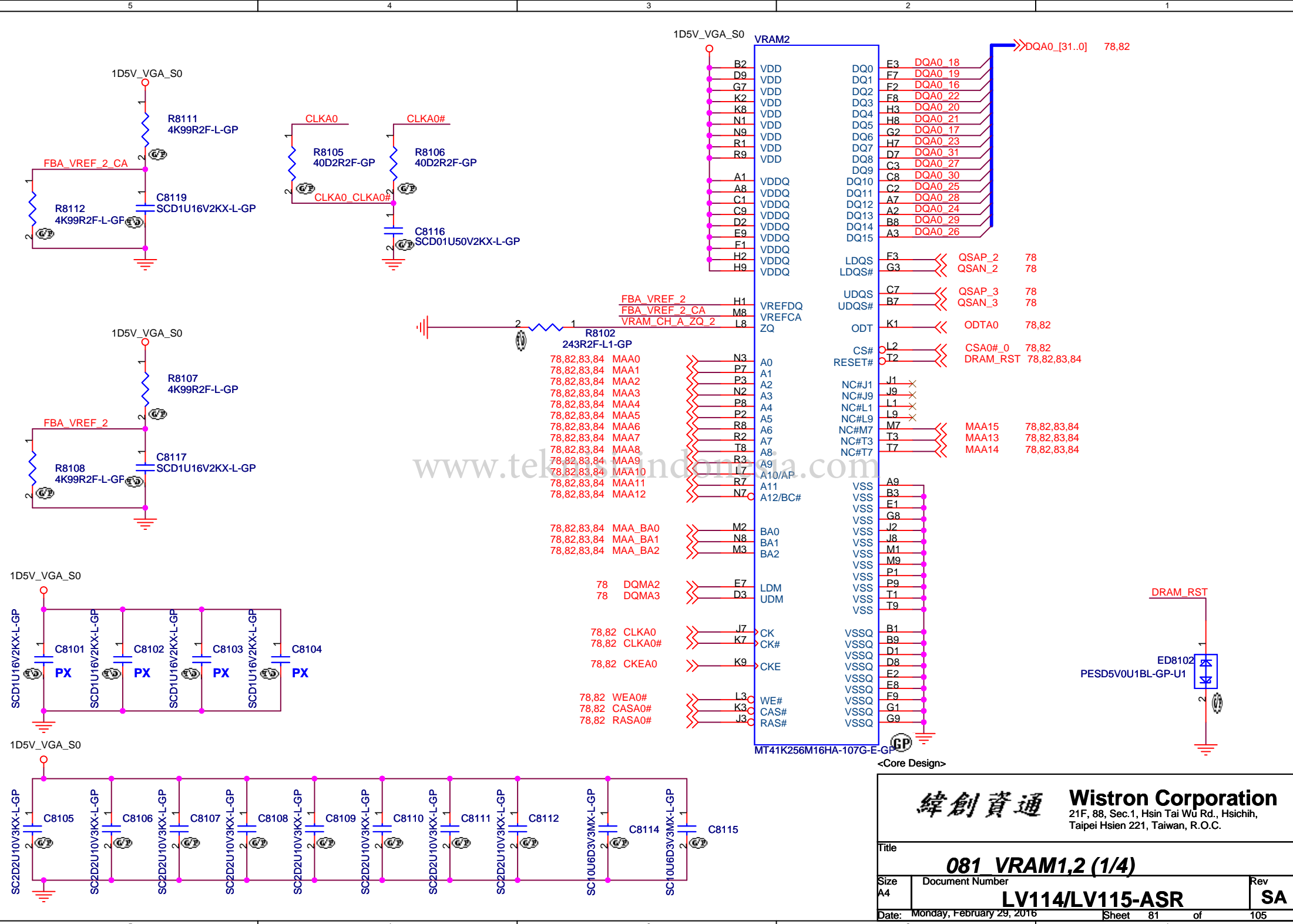


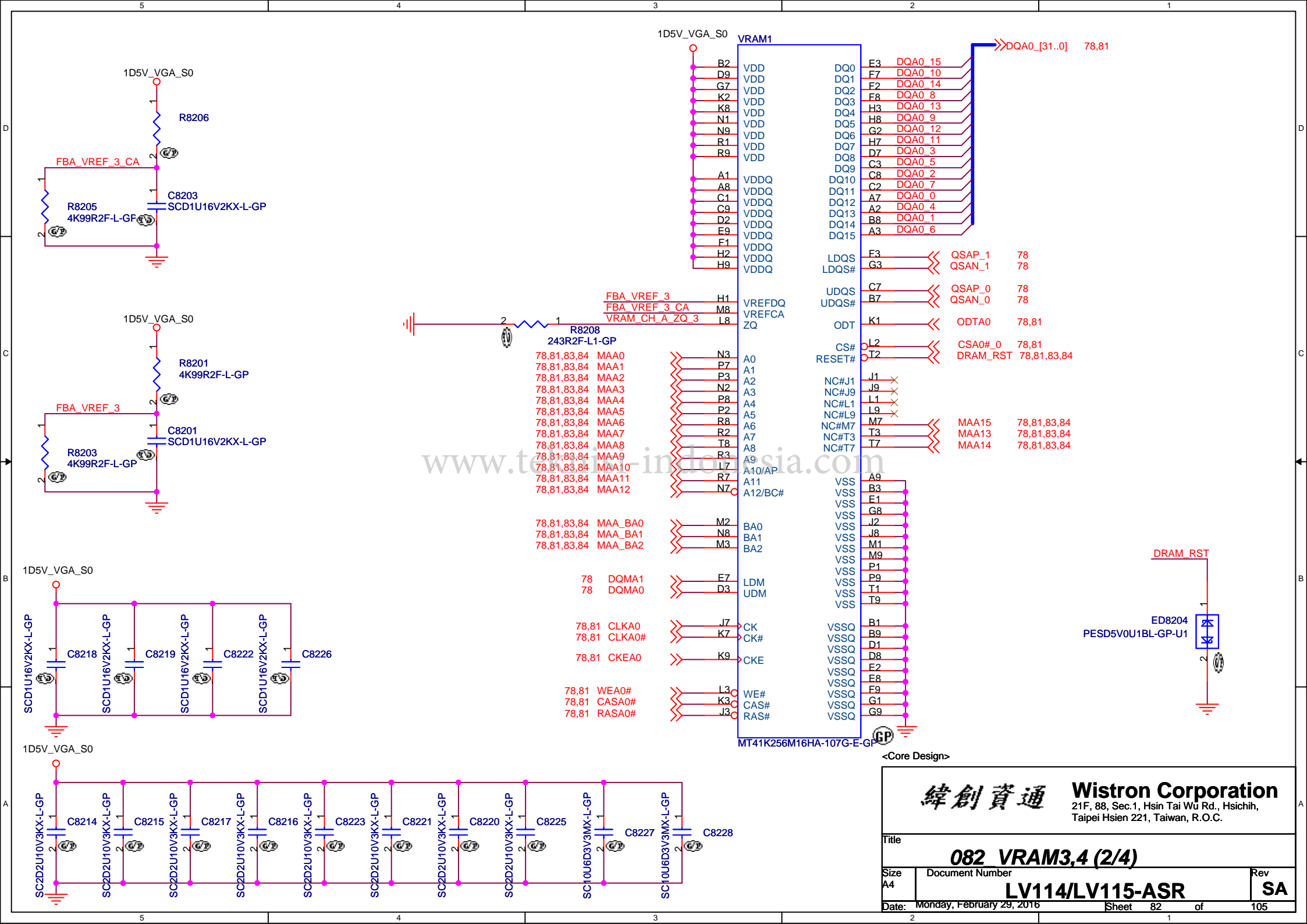












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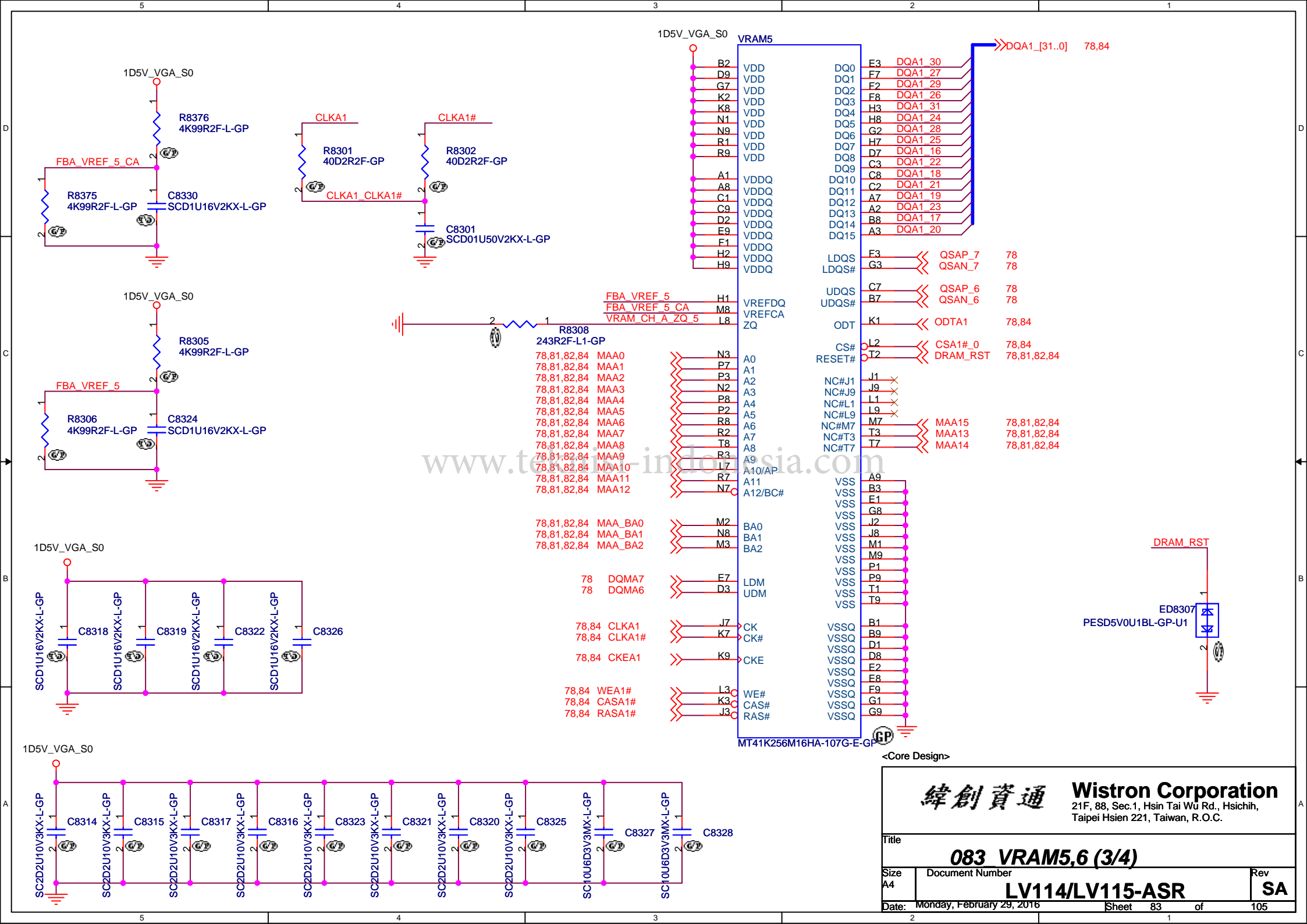
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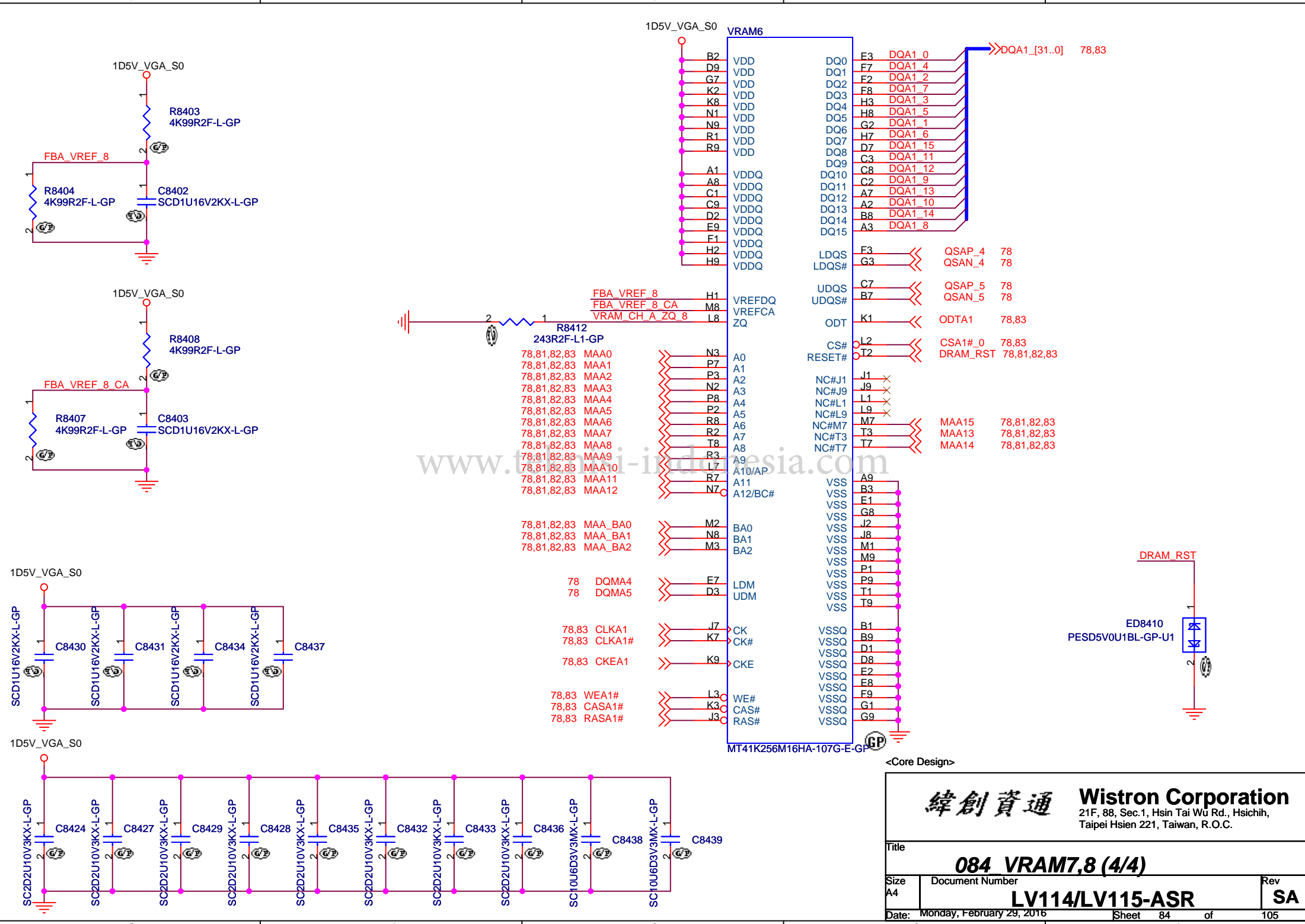
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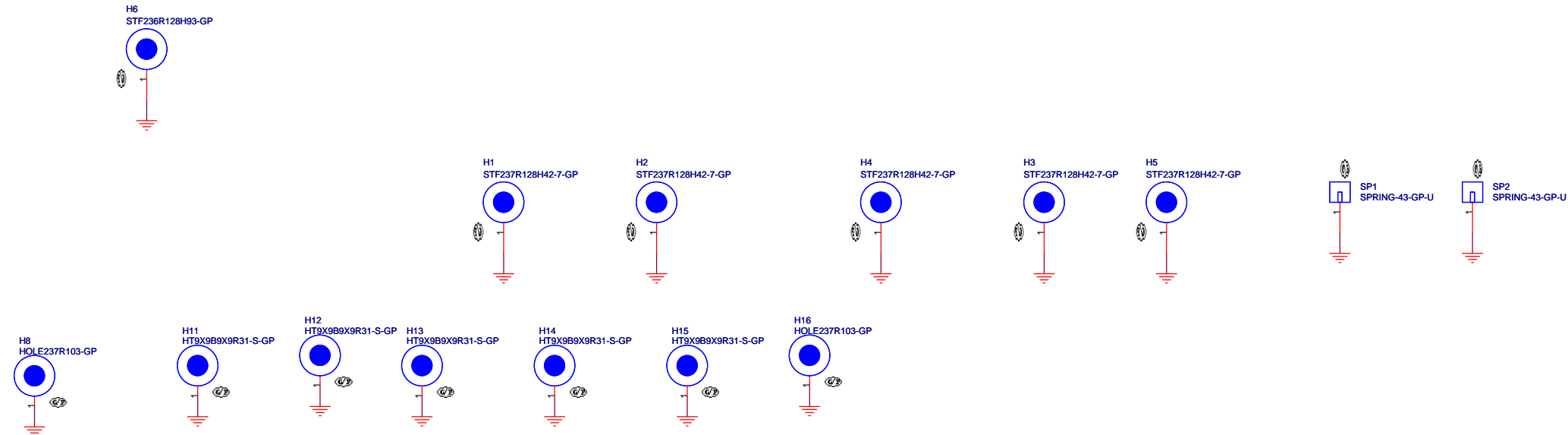


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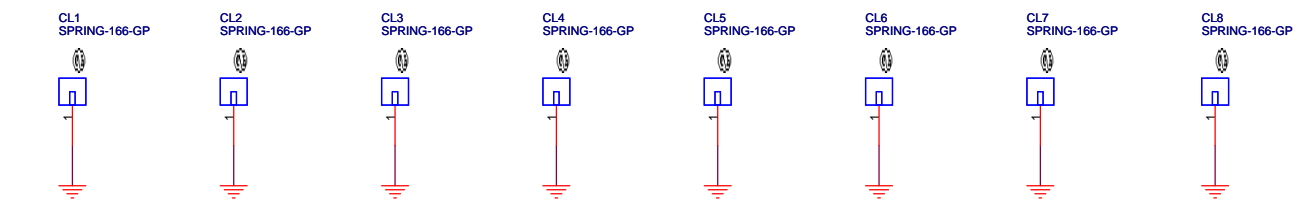
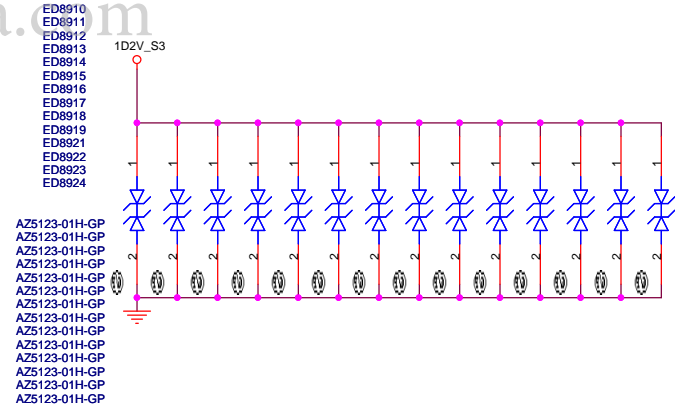
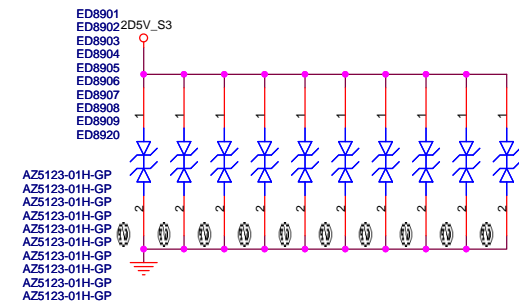
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<b>097 (Reserved) LAN Intel 82579</b>					
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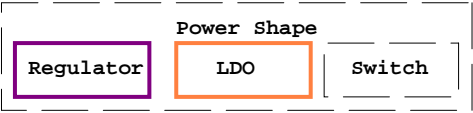
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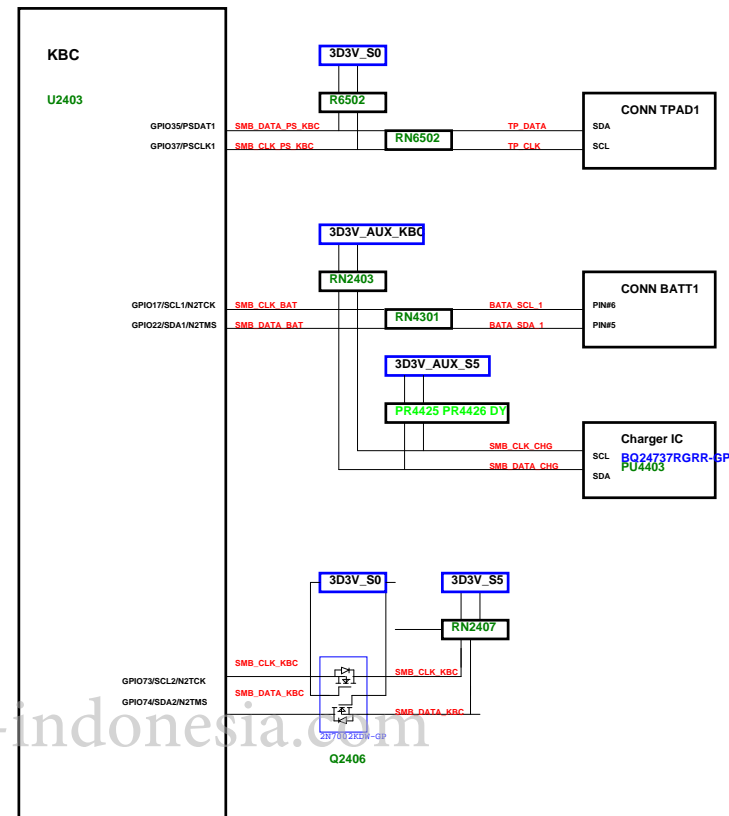
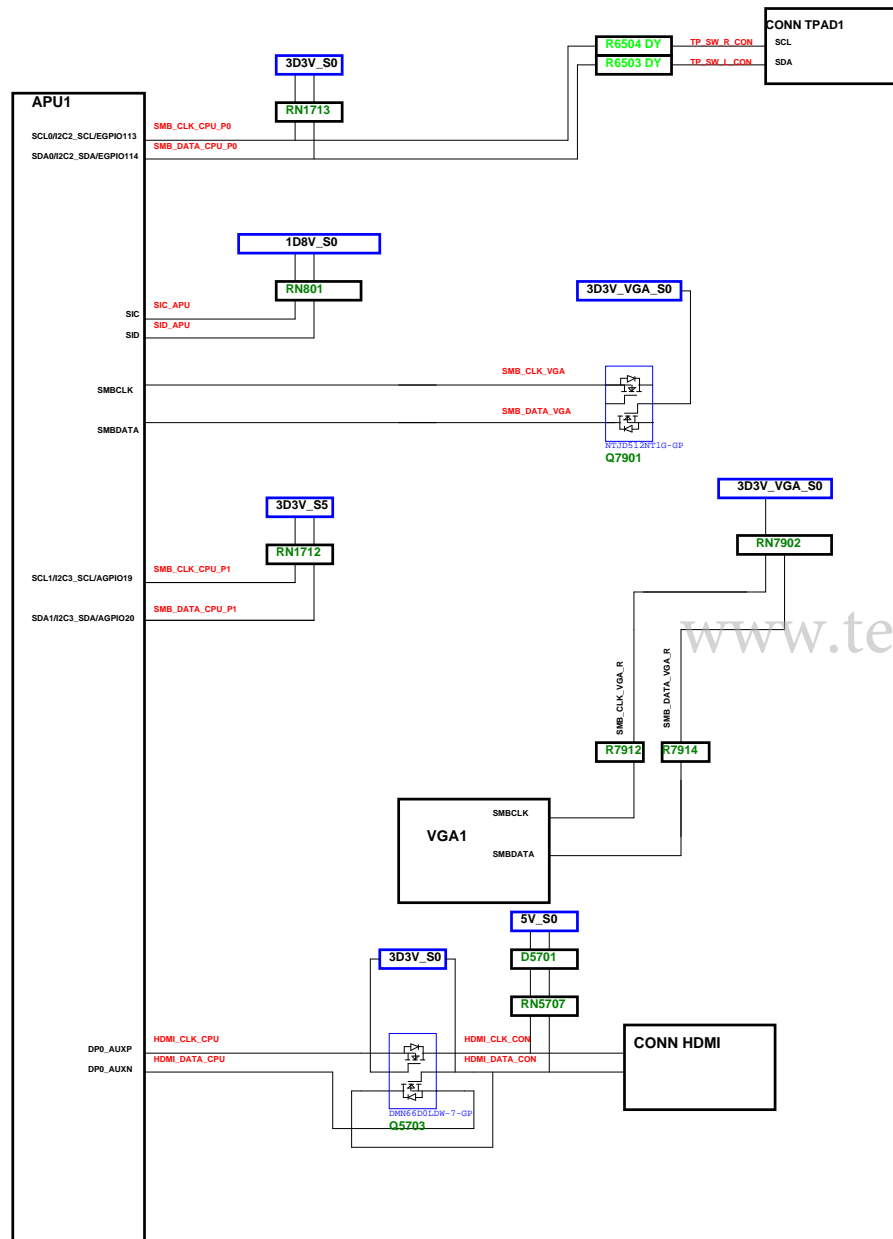
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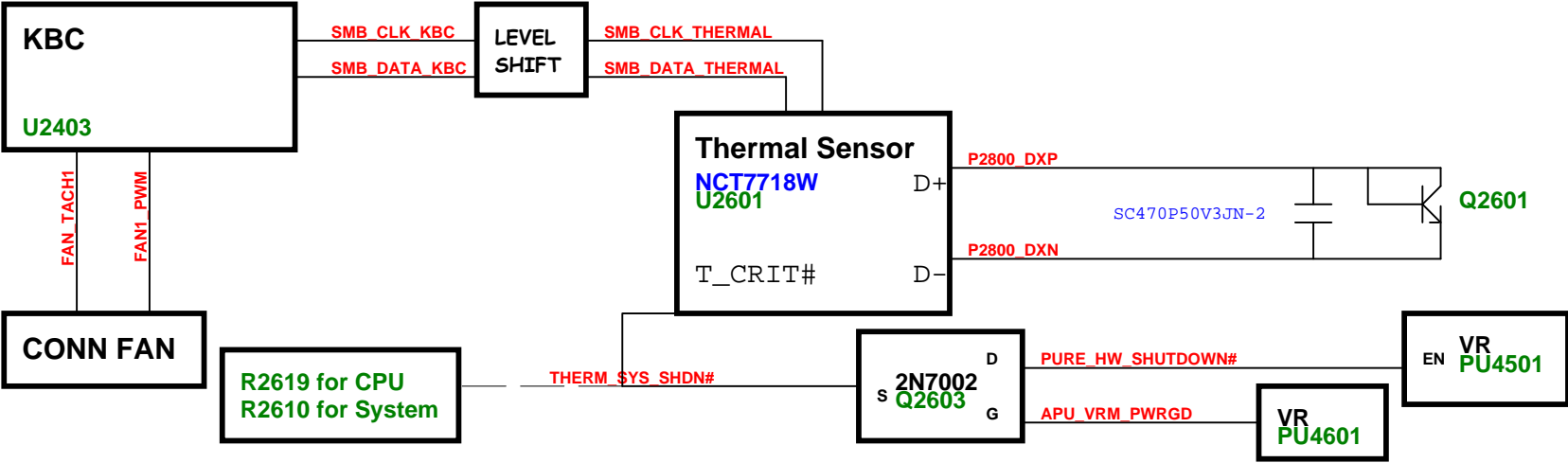
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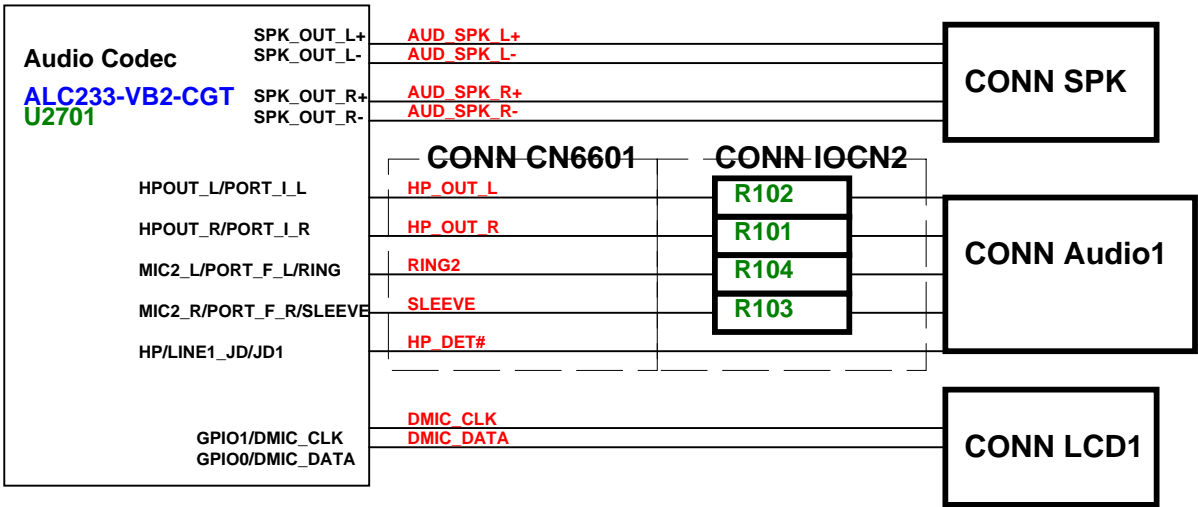


# Thermal Block Diagram



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# Audio Block Diagram



Stoney Ridge + R16M-M1-30

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